

Model Name: GA-B85M-D3H

Revision 2.1

SHEET

TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1150-A
05	CPU_LGA1150-B
06	CPU_LGA1150-C
07	DDR III CHANNEL A 1,2
08	DDR III CHANNEL B 1,2
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*4 SLOT
16	PCI SLOT1,2
17	ITE 8620 LPC IO
18	COM,KB_MS_USB,USB30_20
19	HWM,FAN CTRL,OV,-PROCHOT
20	DUAL BIOS
21	FP,FUSB,SPK,SATALED
22	Realtek ALC892-GR
23	REAR AUDIO JACK
24	REALTEK RTL8111F
25	DISCRETE POWER
26	ATX ,TPM
27	VCORE ISL95812_1

SHEET

TITLE

28	VCORE ISL95812_2
29	RT8120_DDR POWER
30	LPT, M3 POWER
31	DVI, HDMI
32	IT8892E

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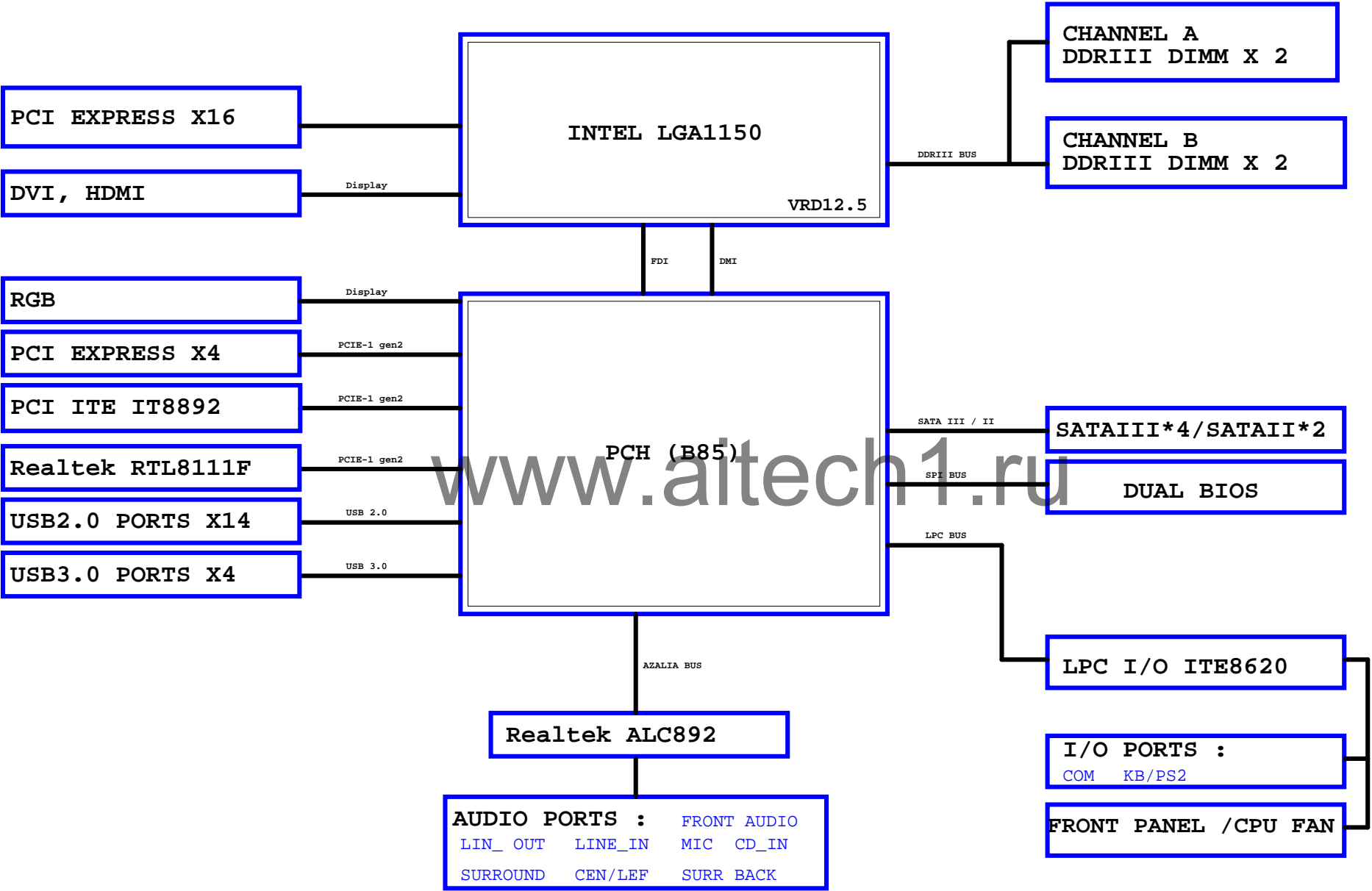
Gigabyte Technology			
Title			
Cover Sheet			
Size	Document Number	GA-B85M-D3H	Rev
Custom			2.1
Date:	Thursday, January 08, 2015	Sheet	1 of 32

Revision 2.1

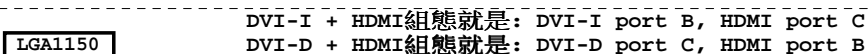
Component value change history

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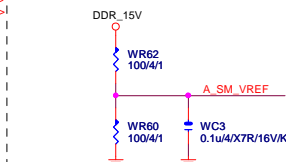
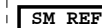
BLOCK DIAGRAM



(D)



CPU PU/PD



THRMTrip DISABLE

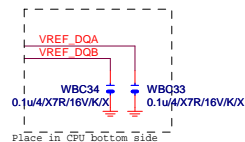
LGA1150 (A)

LGA1150A					
MAAA0	AU13	DDRO_MAO	DDRO_D00	AD38	MDA0
MAAA1	AV16	DDRO_MAO1	DDRO_D01	AD39	MDA1
MAAA2	AU16	DDRO_MAO2	DDRO_D02	AF38	MDA3
MAAA3	AW17	DDRO_MAO3	DDRO_D03	AF39	MDA4
MAAA4	AU17	DDRO_MAO4	DDRO_D04	AD40	MDA5
MAAA5	AW18	DDRO_MAO5	DDRO_D05	AF40	MDA6
MAAA6	AV17	DDRO_MAO6	DDRO_D06	AF41	MDA7
MAAA7	AT18	DDRO_MAO7	DDRO_D07	AD40	MDA9
MAAA8	AU18	DDRO_MAO8	DDRO_D08	AD41	MDA10
MAAA9	AT19	DDRO_MAO9	DDRO_D09	AD42	MDA11
MAAA10	AW110	DDRO_MAO10	DDRO_D10	AD38	MDA10
MAAA11	AV19	DDRO_MAO11	DDRO_D11	AD39	MDA11
MAAA12	AU19	DDRO_MAO12	DDRO_D12	AD38	MDA8
MAAA13	AW19	DDRO_MAO13	DDRO_D13	AD37	MDA14
MAAA14	AT20	DDRO_MAO14	DDRO_D14	AD37	MDA15
MAAA15	AU21	DDRO_MAO15	DDRO_D15	AD37	MDA16
			DDRO_D16	AD38	MDA21
MODT A0	AW10	DDRO_ODT0	DDRO_D17	AD38	MDA18
MODT A1	AY18	DDRO_ODT1	DDRO_D18	AD39	MDA19
MODT A2	AU9	DDRO_ODT2	DDRO_D19	AD37	MDA20
MODT A3	AW8	DDRO_ODT3	DDRO_D20	AD38	MDA16
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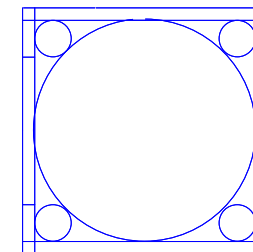
LGA1150 (B)

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	MAAB3	AM23	DDR1_D02
	MAAB4	AP23	DDR1_D03
	MAAB5	AL23	DDR1_D04
	MAAB6	AY24	DDR1_D05
	MAAB7	AV25	DDR1_D06
	MAAB8	AV26	DDR1_D07
	MAAB9	AW26	DDR1_D08
	MAAB10	AP18	DDR1_D09
	MAAB11	AY25	DDR1_D10
	MAAB12	AV26	DDR1_D11
	MAAB13	AP15	DDR1_D12
	MAAB14	AV27	DDR1_D13
	MAAB15	AY28	DDR1_D14
			DDR1_D15
	MODT_B0	AM17	DDR1_D16
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	MODT_B2	AM16	DDR1_D18
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			DDR1_D452
			DDR1_D453



未上件

LGA1150 (CR)

CR
CPU RETENTION/X**LGA1150**

ILM_BP/1156/CSP/ILM_BP/1156/CSP/[12KRC-0F0001-52R_12KRC-0F0001-51R]

DDR BUS

<7> MODT_A[0..3]	MODT_A[0..3]
<8> MODT_B[0..3]	MODT_B[0..3]
<7> MDA[0..63]	MDA[0..63]
<8> MDB[0..63]	MDB[0..63]
<7> DQSA[0..7]	DQSA[0..7]
<7> -DQSA[0..7]	-DQSA[0..7]
<7> MAAA[0..15]	MAAA[0..15]
<8> MAAB[0..15]	MAAB[0..15]
<8> DOSB[0..7]	DOSB[0..7]
<8> -DOSB[0..7]	-DOSB[0..7]

Gigabyte Technology

CPU LGA1150-B

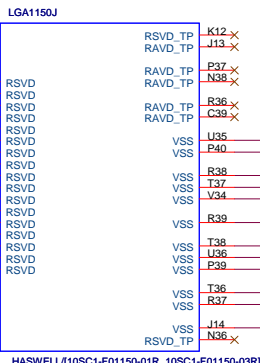
Size Custom	Document Number GA-B85M-D3H	Rev 2.1
Date: Thursday, January 08, 2015	Sheet 5 of 32	

Date: Thursday, January 08, 2015 Sheet 5 of 32

Date: Thursday, January 08, 2015 Sheet 5 of 32

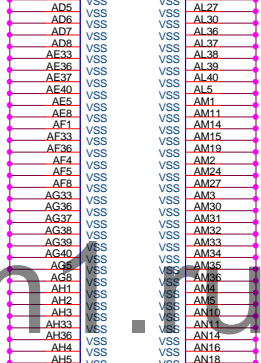
LGA1150

(F,J)



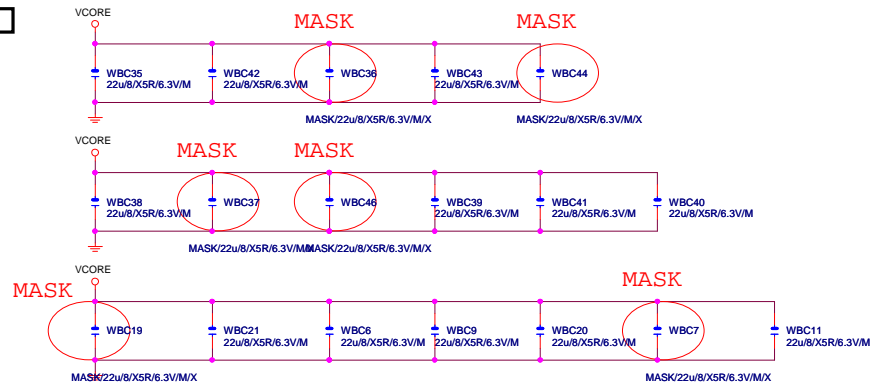
LGA1155

(G,H,I)



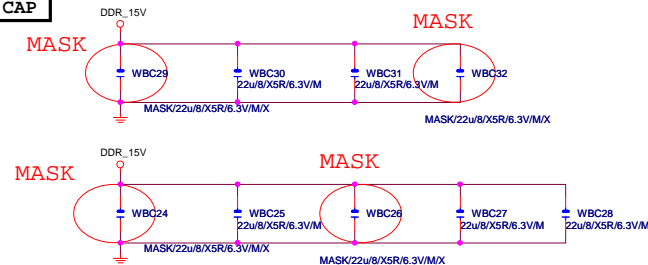
VCore CAP

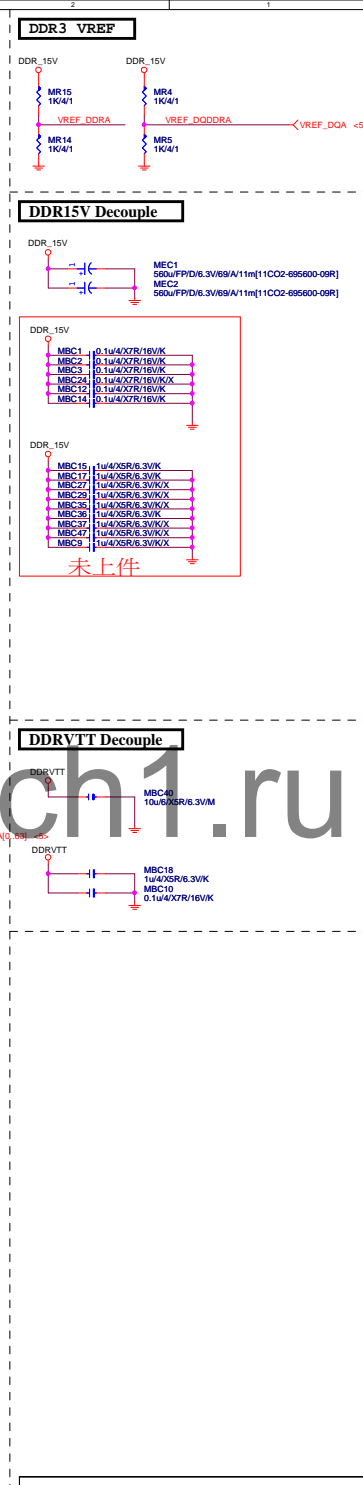
(x18)



DDR CAP

(x9) MASK





PCH

(B)

DMI:12/4/4/12(breakout min 8/4/4/8)
Impedance=85 +- 17.5%

<4> A_DMI_0TXN A_DMI_0TXP L24
<4> A_DMI_0RXN A_DMI_0RXP K24
<4> A_DMI_0RXN A_DMI_0RXP C20
<4> A_DMI_0RXN A_DMI_0RXP B20
<4> A_DMI_1TXN A_DMI_1TXP G24
<4> A_DMI_1TXN A_DMI_1TXP H24
<4> A_DMI_1RXN A_DMI_1RXP D21
<4> A_DMI_1RXN A_DMI_1RXP B21
<4> A_DMI_2TXN A_DMI_2TXP F26
<4> A_DMI_2TXN A_DMI_2TXP B22
<4> A_DMI_2RXN A_DMI_2RXP C22
<4> A_DMI_2RXN A_DMI_2RXP K26
<4> A_DMI_3TXN A_DMI_3TXP L26
<4> A_DMI_3TXN A_DMI_3TXP A24
<4> A_DMI_3RXN A_DMI_3RXP A24
<4> A_DMI_3RXN A_DMI_3RXP B24

W=4 mil out of PCH
S=15 mil out of PCH

VCC1_5_PCH NR50 7.5K/4/1 DMI_COMP B19
NR40 7.5K/4/1 PCIE_COMP C13
<10> CK_SRCCLK_PCH CK_SRCCLK_PCH G22
<10> CK_SRCCLK_PCH CK_SRCCLK_PCH F22

8111F

8892

PCIEx4

電容放靠近 Device & PCI-E Slot

PCIEX1:15/4/4/15 (breakout min 8/4/4/8)
Impedance=85 +- 17.5%

PCH

(J)

PCH PCIE ,DMI 15/4/4/15

usb2.0 12/5/7/5/12
usb3.0 20/5/7/5/20

AT1
AT41
AU1
AV1
AV2
AV40
AV41
AW2
AW40
B40
B41
C41
D1
D41

PCHJ

TP22 U11
TP23 U10
TP21 A14
TP20 AK14
TP14 K34
TP15 K33
TP12 AH24
TP10 L16
TP11 K16
TP9 AM34
TP3 R12
TP4 N12
TP1 L22
TP2 K22
TP5 R4
TP6 K5
TP7 P5
TP8 L5
VSS AC31
VSS AF3
VSS AV21

DH82B85/S[10HB1-030B85-20R]

PORT1,PORT9[DEBUG PORT]FOR WHQL一定要拉出PORT

B85: Port 6/7 N/A
H81: Port 6/7/12/13 N/A

PCHB

USBN_0 N_USBP0
USBN_0 N_USBP0
USBN_1 N_USBP1
USBN_1 N_USBP1
USBN_2 N_USBP2
USBN_2 N_USBP2
USBN_3 N_USBP3
USBN_3 N_USBP3
USBN_4 N_USBP4
USBN_4 N_USBP4
USBN_5 N_USBP5
USBN_5 N_USBP5
USBN_6 N_USBP6
USBN_6 N_USBP6
USBN_7 N_USBP7
USBN_7 N_USBP7
USBN_8 N_USBP8
USBN_8 N_USBP8
USBN_9 N_USBP9
USBN_9 N_USBP9
USBN_10 N_USBP10
USBN_10 N_USBP10
USBN_11 N_USBP11
USBN_11 N_USBP11
USBN_12 N_USBP12
USBN_12 N_USBP12
USBN_13 N_USBP13
USBN_13 N_USBP13

OC0B_GP59 AE40
OC1B_GP40 AF37
OC2B_GP41 AD33
OC3B_GP42 AE39
OC4B_GP43 AC41
OC5B_GP9 AF40
OC6B_GP10 AG40
OC7B_GP14 N_GPIO14
USBRBIAS NR47 22.6/4/1
USBRBIAS NR47 22.6/4/1
AP11 CK_DOTCLK
AM11 CK_DOTCLK

CLKIN_DOT96N
CLKIN_DOT96P
N_USBOC_F
N_USBOC_R
NBC82 0.1u/4X7R/16V/K
NBC83 0.1u/4X7R/16V/K

PCH

(F)

<21> PCH_USB3_RXN0
<21> PCH_USB3_RXP0
<21> PCH_USB3_TXN0
<21> PCH_USB3_TXP0
<21> PCH_USB3_RXN1
<21> PCH_USB3_RXP1
<21> PCH_USB3_TXN1
<21> PCH_USB3_TXP1
<18> PCH_USB3_RXN4
<18> PCH_USB3_RXP4
<18> PCH_USB3_TXN4
<18> PCH_USB3_TXP4
<18> PCH_USB3_RXN5
<18> PCH_USB3_RXP5
<18> PCH_USB3_TXN5
<18> PCH_USB3_TXP5

PCHF

USB3 FDI LINK
USB3_RXN_0 FDI_RXN_0
USB3_RXP_0 FDI_RXP_0
USB3_TXN_0 FDI_TXN_0
USB3_TXP_0 FDI_TXP_0
USB3_RXN_1 FDI_RXN_1
USB3_RXP_1 FDI_RXP_1
USB3_TXN_1 FDI_TXN_1
USB3_TXP_1 FDI_TXP_1
USB3_RXN_4 FDI_RXN_4
USB3_RXP_4 FDI_RXP_4
USB3_TXN_4 FDI_TXN_4
USB3_TXP_4 FDI_TXP_4
USB3_RXN_5 FDI_RXN_5
USB3_RXP_5 FDI_RXP_5
USB3_TXN_5 FDI_TXN_5
USB3_TXP_5 FDI_TXP_5

N1 FDI_TXN0
N2 FDI_TXP0
N3 FDI_TXN1
N4 FDI_TXP1
L2 FDI_CSYSN0
L3 FDI_INT
K2 NR29 7.5K/4/1 VCC1_5_PCH

未上件

USB3.0:20/5/7/5/20 (breakout min 8/4/4/8) ; ONLY 3 VIAS
Impedance=85 +- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

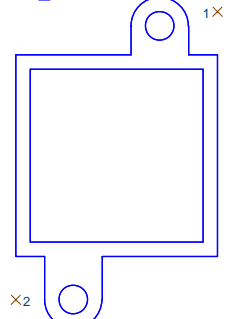
PCH CLK PD

<10> N_PCHCLK14

N_PCHCLK14
CK_DOTCLK
CK_DOTCLK

PCH H/S

SB_HEATSIN

PCH_HS
PCH_HS[12SP2-S04208-61R_12SP2-S04208-62R_12SP2-S04208-63R]

USB TABLE

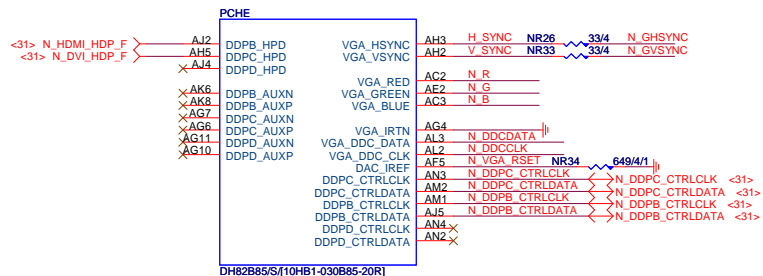
OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)

USB OC#	Configure
OC0#	F_USB30
OC1#	R_USB30
OC2#	USB30_LAN
OC3#	F_USB3
OC4#	F_USB2
OC5#	KB_MS_USB
OC6#	F_USB1
OC7#	Not Use

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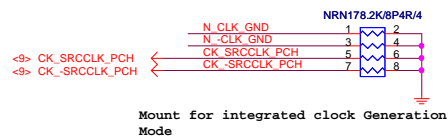
Title PCH FDI,DMI,USB ,PCIE,NVRAM		
Size Custom	Document Number GA-B85M-D3H	Rev 2.1
Date: Thursday, January 08, 2015	Sheet 9	of 32

PCH (E)

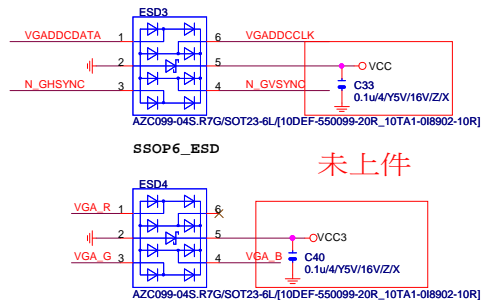


VGA_DISABLE
R,G,B NC OR GND
IRTN / IREF GND
VGA_HSYNC, VGA_VSYNC, DDC_CLK, DDC_DATA NC
POWER VCCADAC(AF2), VCCADACBG(AE1) GND

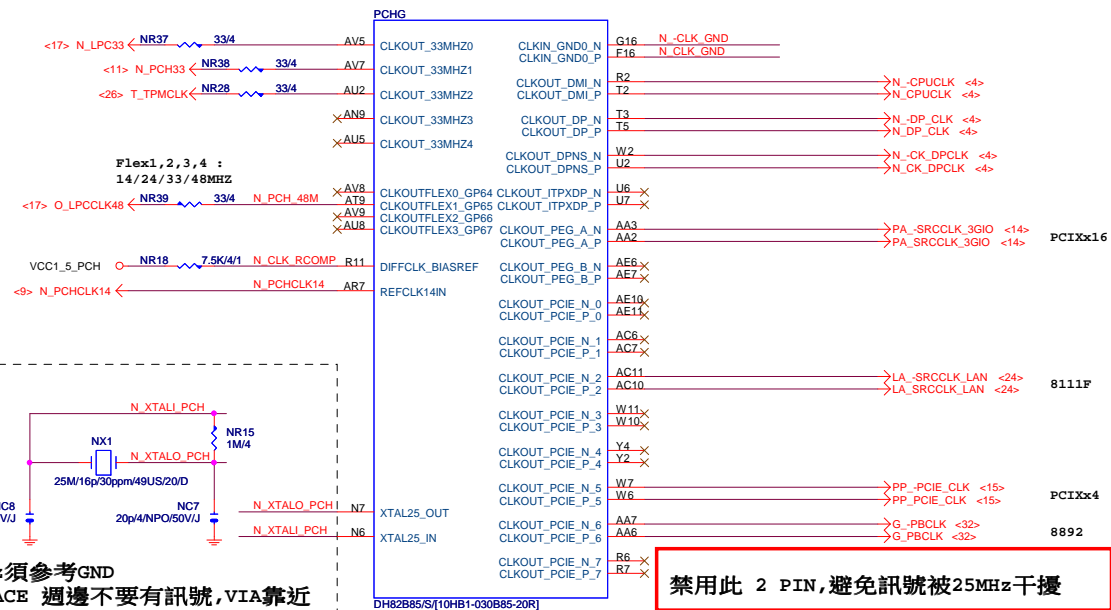
PCH CLK PD



VGA ESD

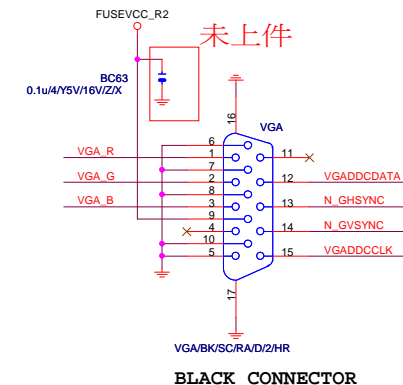


PCH (G)

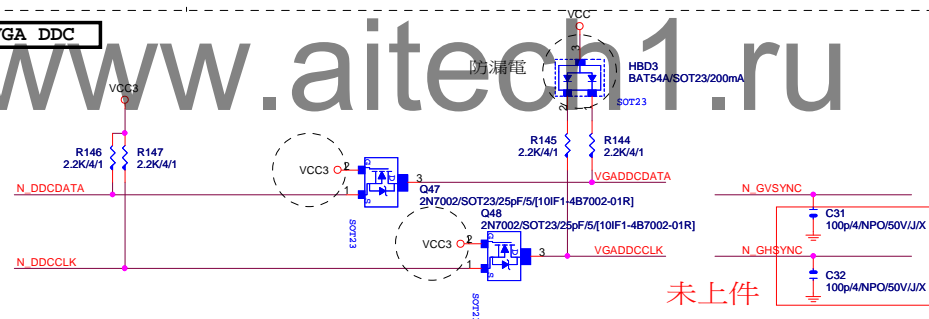


Differential Clock:18/4/6/4/18
Impedance=90 +- 15%

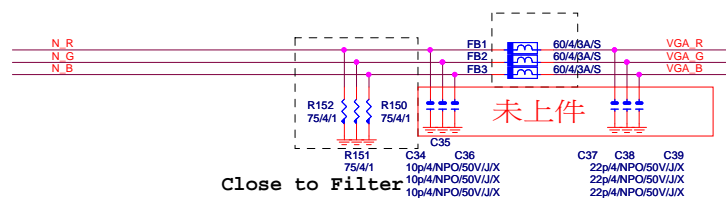
VGA CONNECTOR



VGA DDC

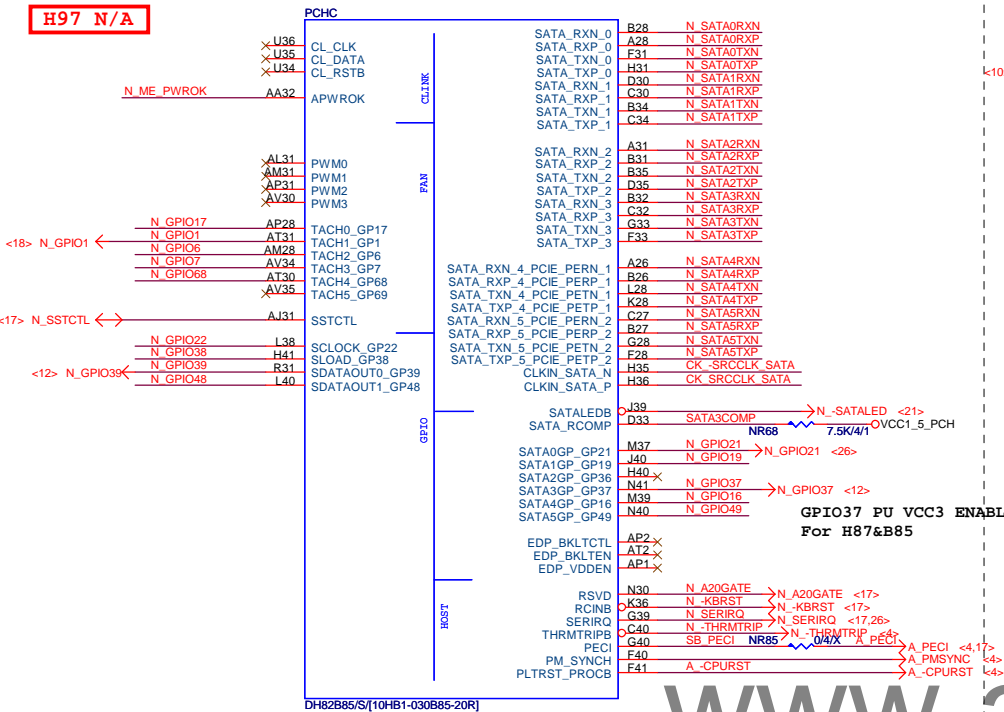


VGA DDC

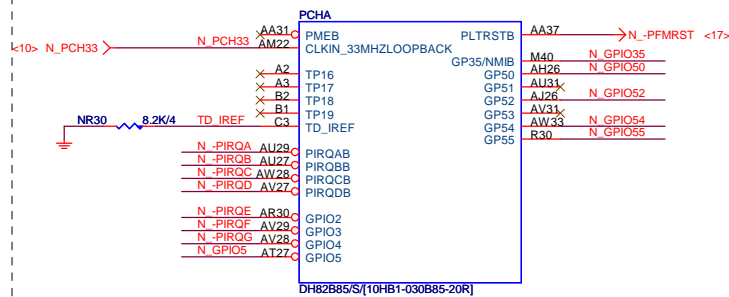


H97 N/A

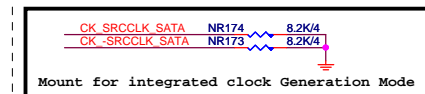
SATA3 : 20/7.5/4.5/7.5/20 (breakout min 8/4/4/4/8)
Impedance=90 +/- 17.5%
SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)
Impedance=90 +/- 17.5%



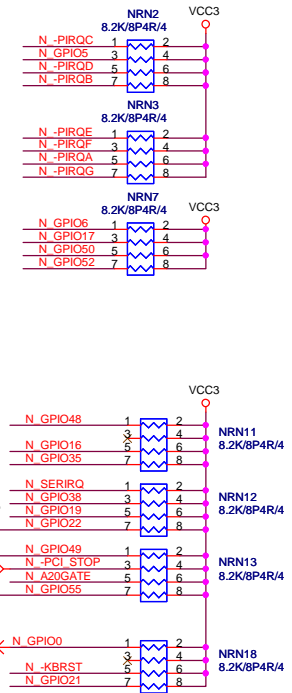
PCH (A)



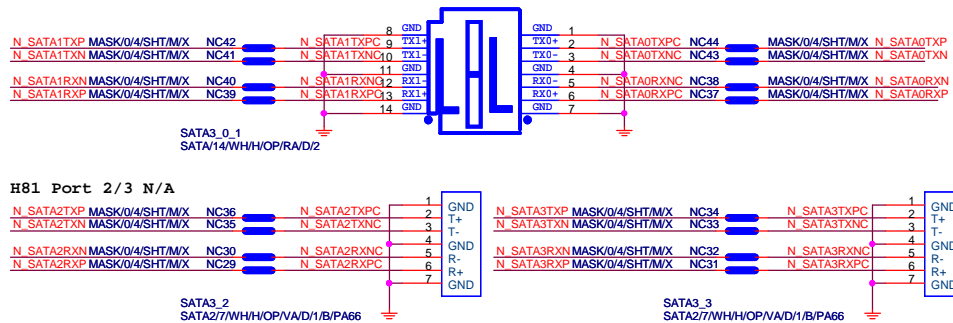
PCH	CLK	PD
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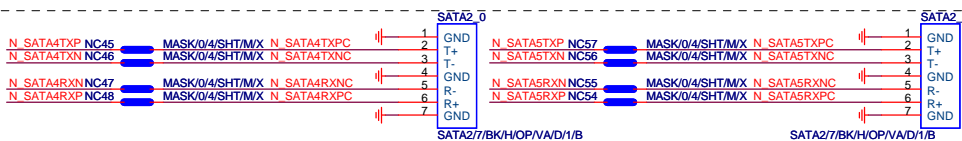
PCH	PU/PD
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SATA CONNECTOR

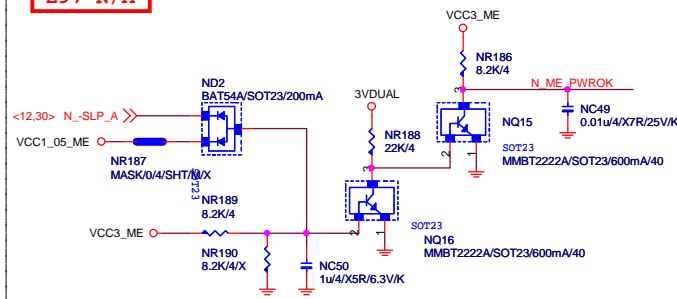


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** Z87/H87 Port 4&5 SATA3.0
** B85 Port 4&5 SATA2.0
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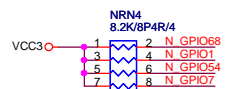


ME PWROK

Z97 N/A



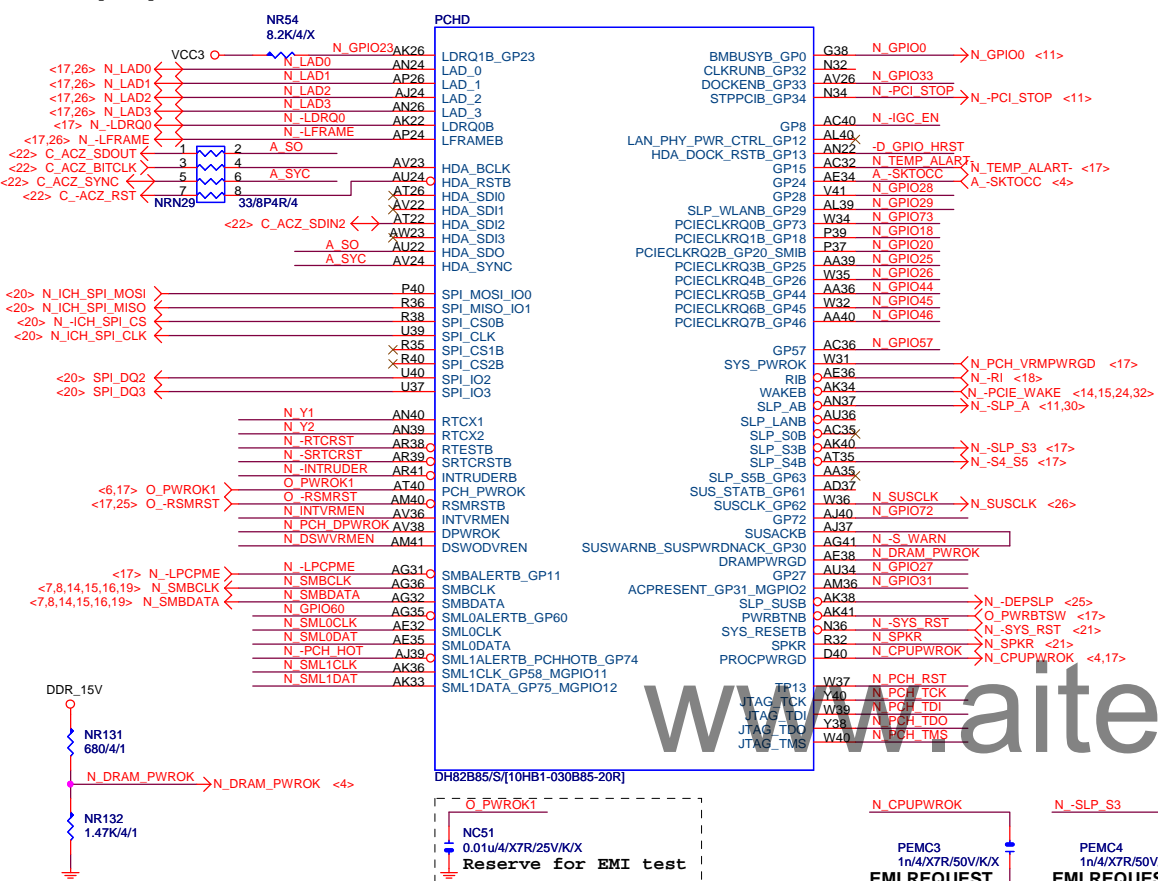
GPIO38 Ctrl



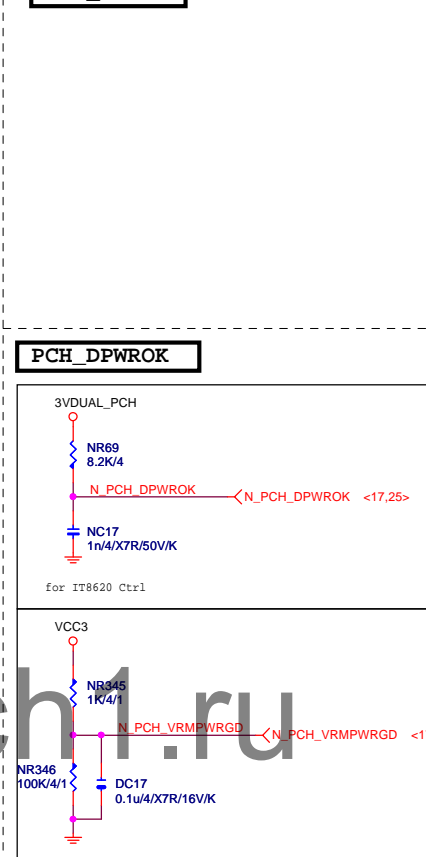
Gigabyte Technology

Title			
PCH HOST , SATA, PCI			
Size	Document Number	Rev	
Custom	GA-B85M-D3H	2.1	
Date:	Thursday, January 08, 2015	Sheet	11 of 32

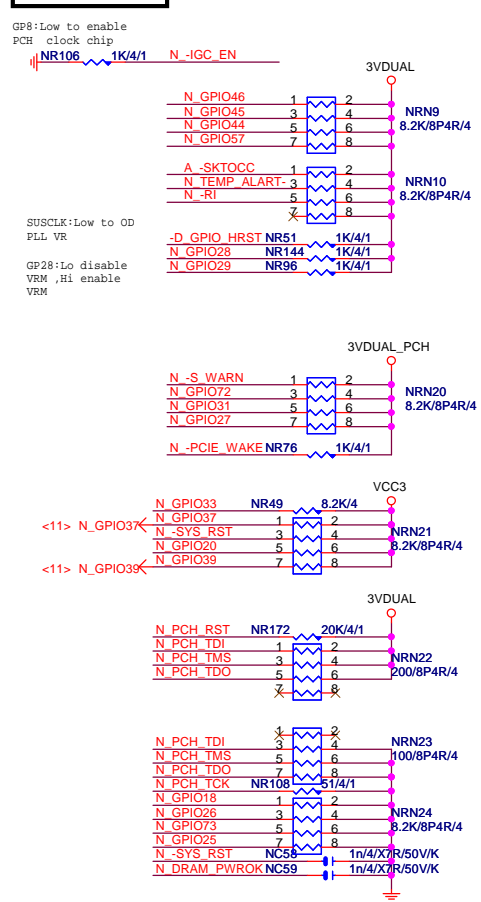
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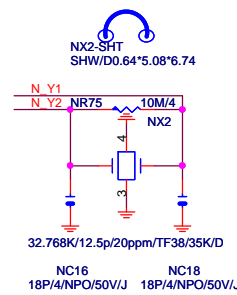
ACZ_SDOUT



PCH	PU/PD
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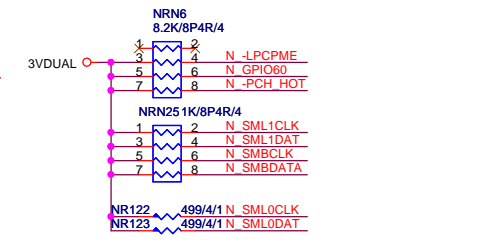
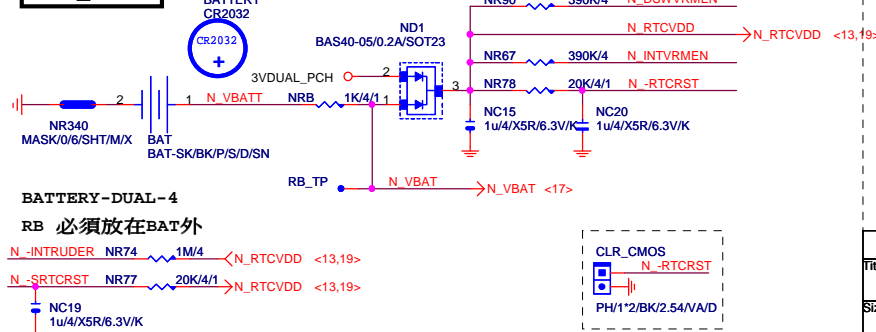


HSW_STRAP13



32.768KHZ

CLR_CMOS



Gigabyte Technology

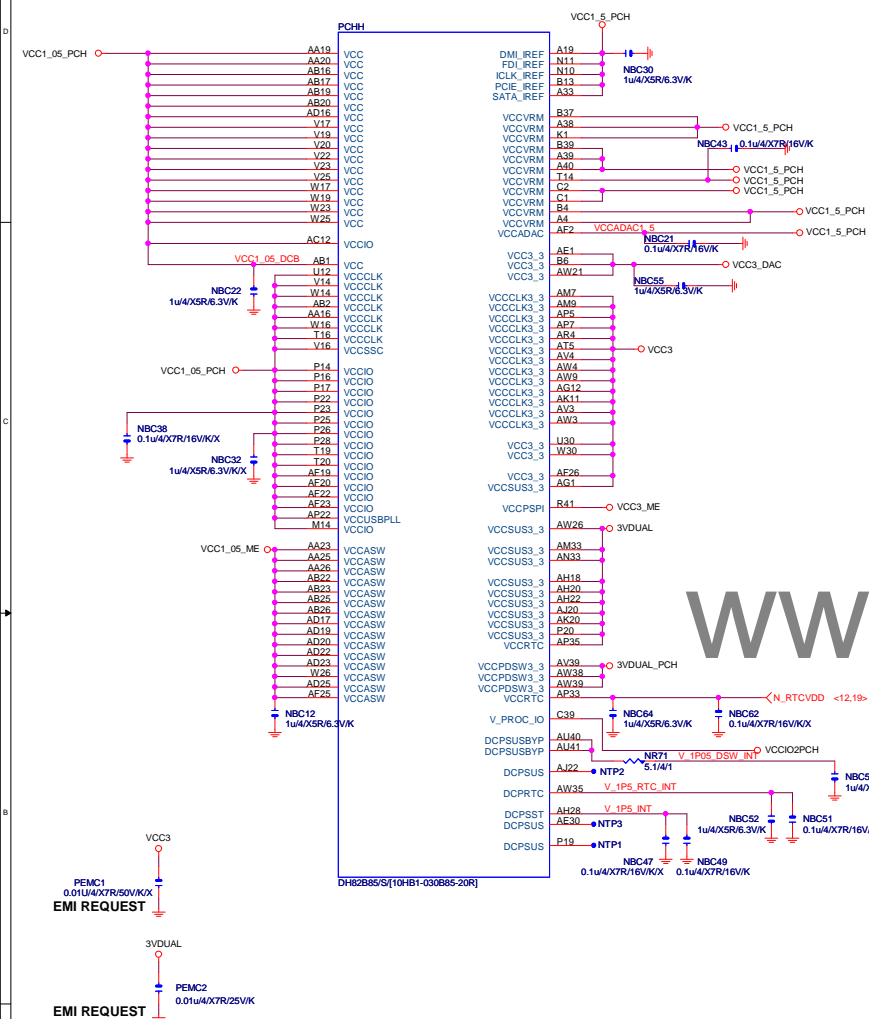
PCH GPIO , CTRL , AUDIO

Size Custom	Document Number GA-B85M-D3H	Rev 2.1
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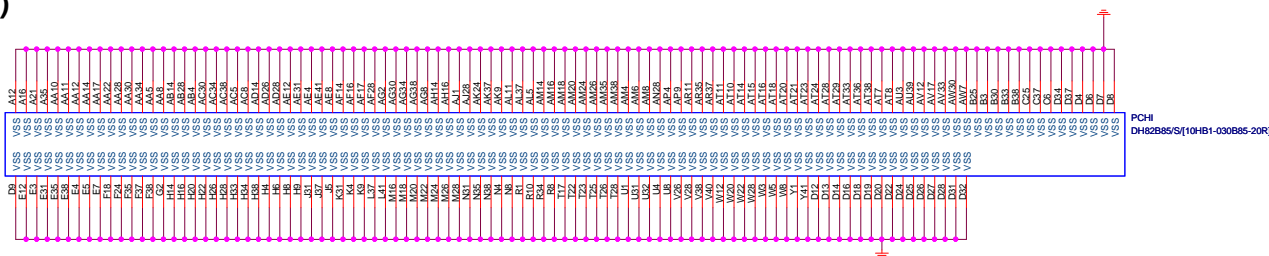
Date: Thursday, January 08, 2015 Sheet 12 of 32

	1
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PCH (H)



PCH (I)



VCC3_DAC

3VDUAL_PCH

SHT PWR

H97 N/A

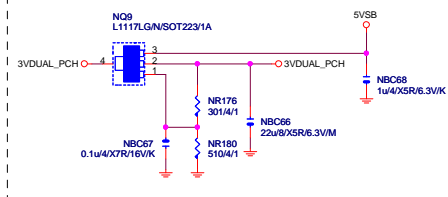
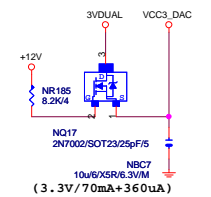
MASK

R1.0

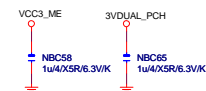
SHORT WIRE

[Z97]

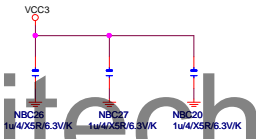
CLOSE北橋(注意震盪水波紋)



CAP

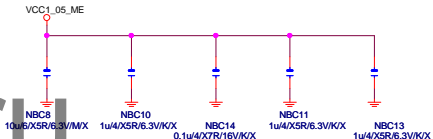


(3.3V) (X6)

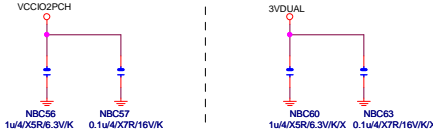


(1.05V) (x6)

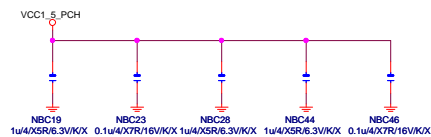
(1.05V) (x5)



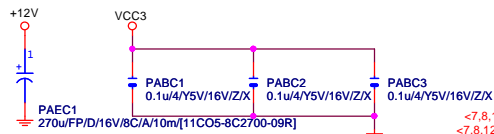
(1.05V)(x2) (3.3V) (x2)



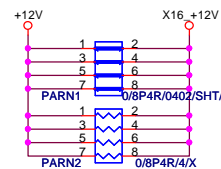
(1.05V) (x10)



PCIEX16 CAP



PCIEX16	PROTECT	SHT
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PCIEX16	AC	CAP
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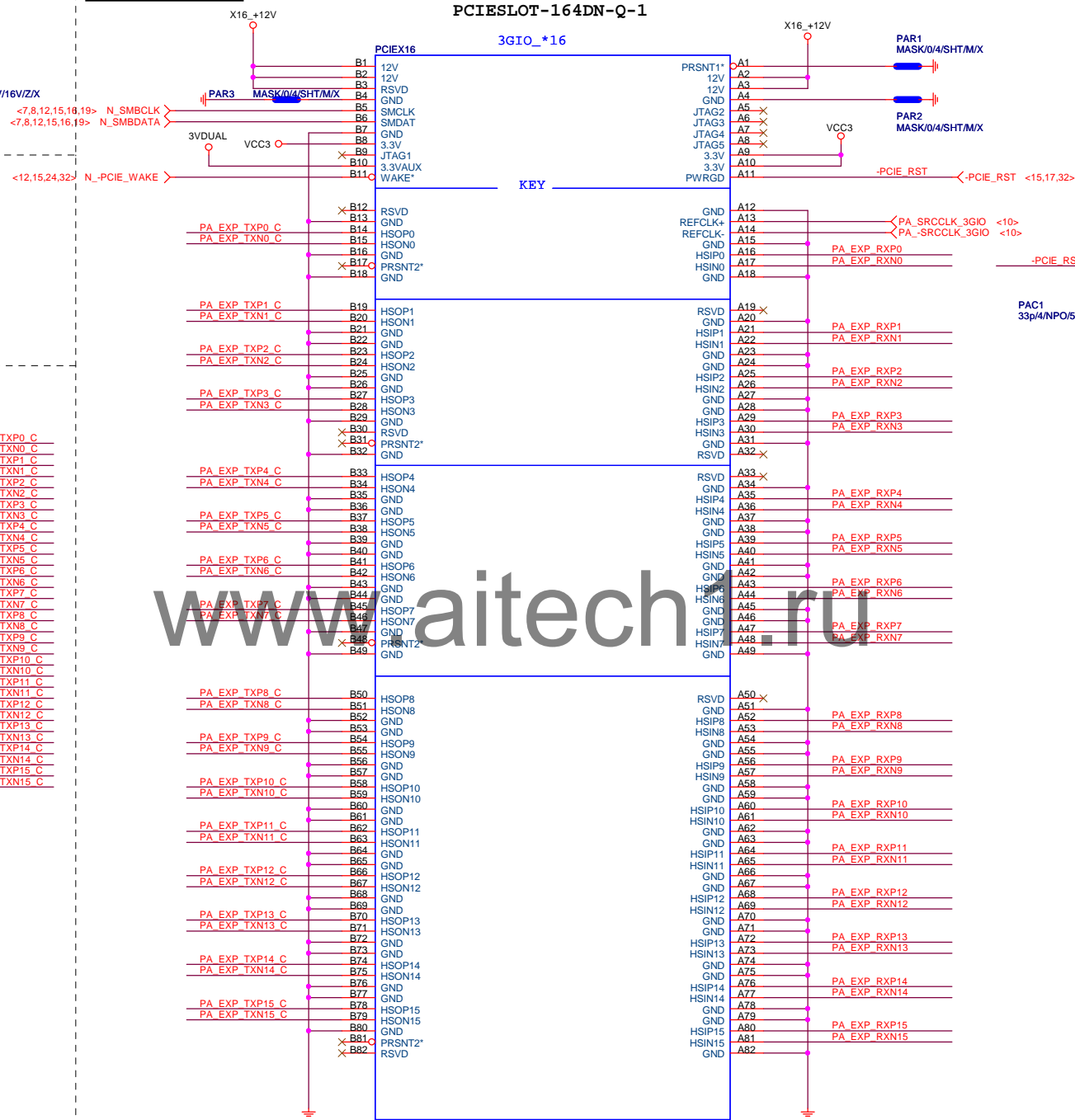
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PA EXP TXN0	PAC4	0.22u4/X5R6.3V/K	PA EXP TXN0 C
PA EXP TXP1	PAC6	0.22u4/X5R6.3V/K	PA EXP TXP1 C
PA EXP TXN1	PAC7	0.22u4/X5R6.3V/K	PA EXP TXN1 C
PA EXP TXP2	PAC8	0.22u4/X5R6.3V/K	PA EXP TXP2 C
PA EXP TXN2	PAC9	0.22u4/X5R6.3V/K	PA EXP TXN2 C
PA EXP TXP3	PAC10	0.22u4/X5R6.3V/K	PA EXP TXP3 C
PA EXP TXN3	PAC11	0.22u4/X5R6.3V/K	PA EXP TXN3 C
PA EXP TXP4	PAC12	0.22u4/X5R6.3V/K	PA EXP TXP4 C
PA EXP TXN4	PAC13	0.22u4/X5R6.3V/K	PA EXP TXN4 C
PA EXP TXP5	PAC14	0.22u4/X5R6.3V/K	PA EXP TXP5 C
PA EXP TXN5	PAC15	0.22u4/X5R6.3V/K	PA EXP TXN5 C
PA EXP TXP6	PAC16	0.22u4/X5R6.3V/K	PA EXP TXP6 C
PA EXP TXN6	PAC17	0.22u4/X5R6.3V/K	PA EXP TXN6 C
PA EXP TXP7	PAC18	0.22u4/X5R6.3V/K	PA EXP TXP7 C
PA EXP TXN7	PAC19	0.22u4/X5R6.3V/K	PA EXP TXN7 C
PA EXP TXP8	PAC20	0.22u4/X5R6.3V/K	PA EXP TXP8 C
PA EXP TXN8	PAC21	0.22u4/X5R6.3V/K	PA EXP TXN8 C
PA EXP TXP9	PAC22	0.22u4/X5R6.3V/K	PA EXP TXP9 C
PA EXP TXN9	PAC23	0.22u4/X5R6.3V/K	PA EXP TXN9 C
PA EXP TXP10	PAC24	0.22u4/X5R6.3V/K	PA EXP TXP10 C
PA EXP TXN10	PAC25	0.22u4/X5R6.3V/K	PA EXP TXN10 C
PA EXP TXP11	PAC26	0.22u4/X5R6.3V/K	PA EXP TXP11 C
PA EXP TXN11	PAC27	0.22u4/X5R6.3V/K	PA EXP TXN11 C
PA EXP TXP12	PAC28	0.22u4/X5R6.3V/K	PA EXP TXP12 C
PA EXP TXN12	PAC29	0.22u4/X5R6.3V/K	PA EXP TXN12 C
PA EXP TXP13	PAC30	0.22u4/X5R6.3V/K	PA EXP TXP13 C
PA EXP TXN13	PAC31	0.22u4/X5R6.3V/K	PA EXP TXN13 C
PA EXP TXP14	PAC32	0.22u4/X5R6.3V/K	PA EXP TXP14 C
PA EXP TXN14	PAC33	0.22u4/X5R6.3V/K	PA EXP TXN14 C
PA EXP TXP15	PAC34	0.22u4/X5R6.3V/K	PA EXP TXP15 C
PA EXP TXN15	PAC35	0.22u4/X5R6.3V/K	PA EXP TXN15 C

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PCIEX16 SLOT



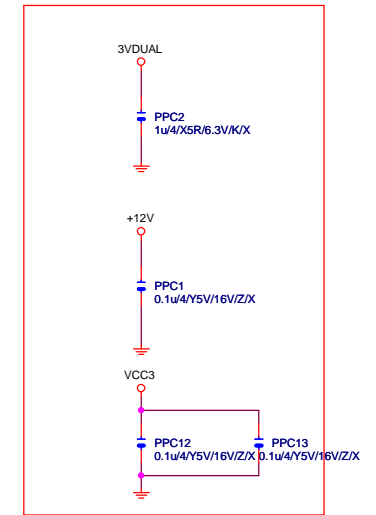
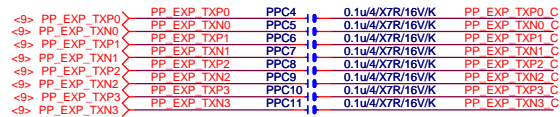
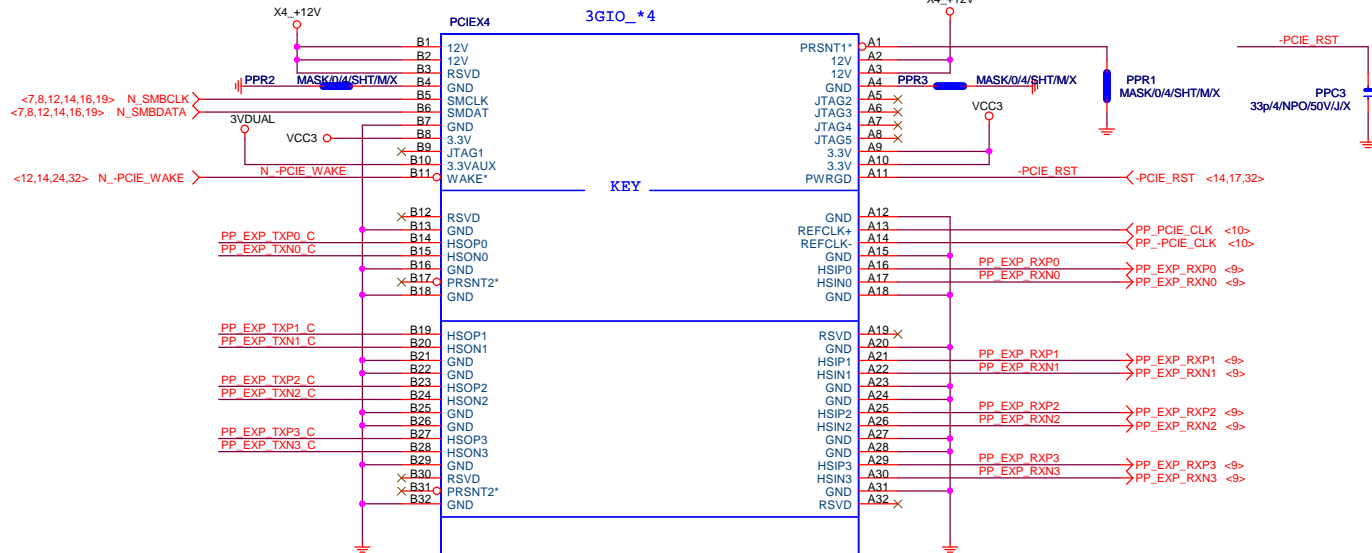
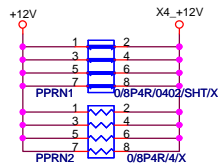
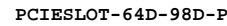
PCI-E/16X-164P/BK/LONG DOUBLE

BLACK CONNECTOR

Gigabyte Technology

Title				PCI EXPRESS * 16			
Size Custom		Document Number				Rev	
		GA-B85M-D3H				2.	
Date:		Thursday, January 08, 2015		Sheet		14 of 32	

PCIEX4 SLOT

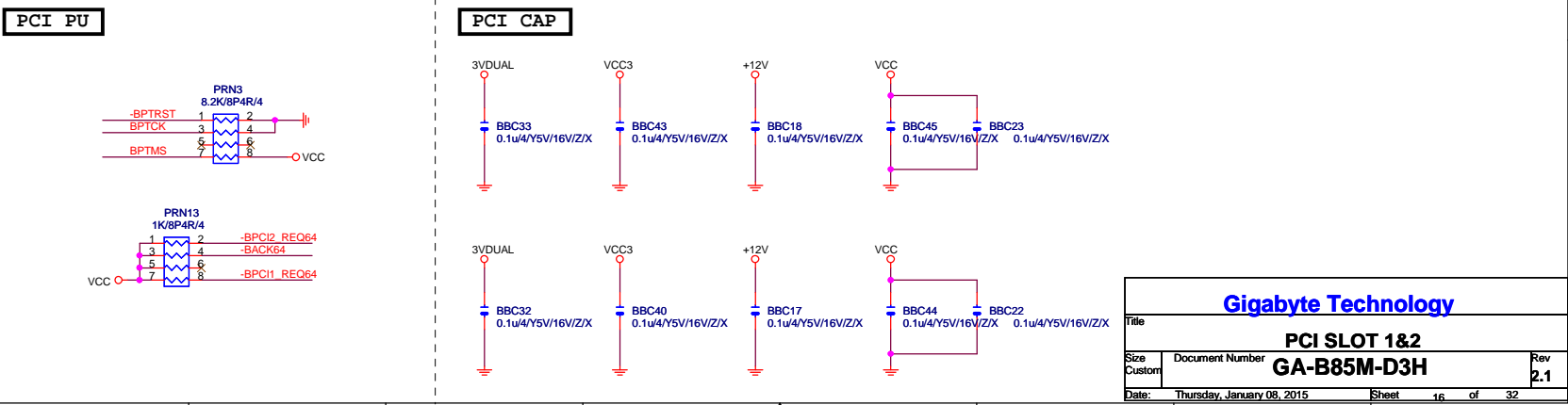
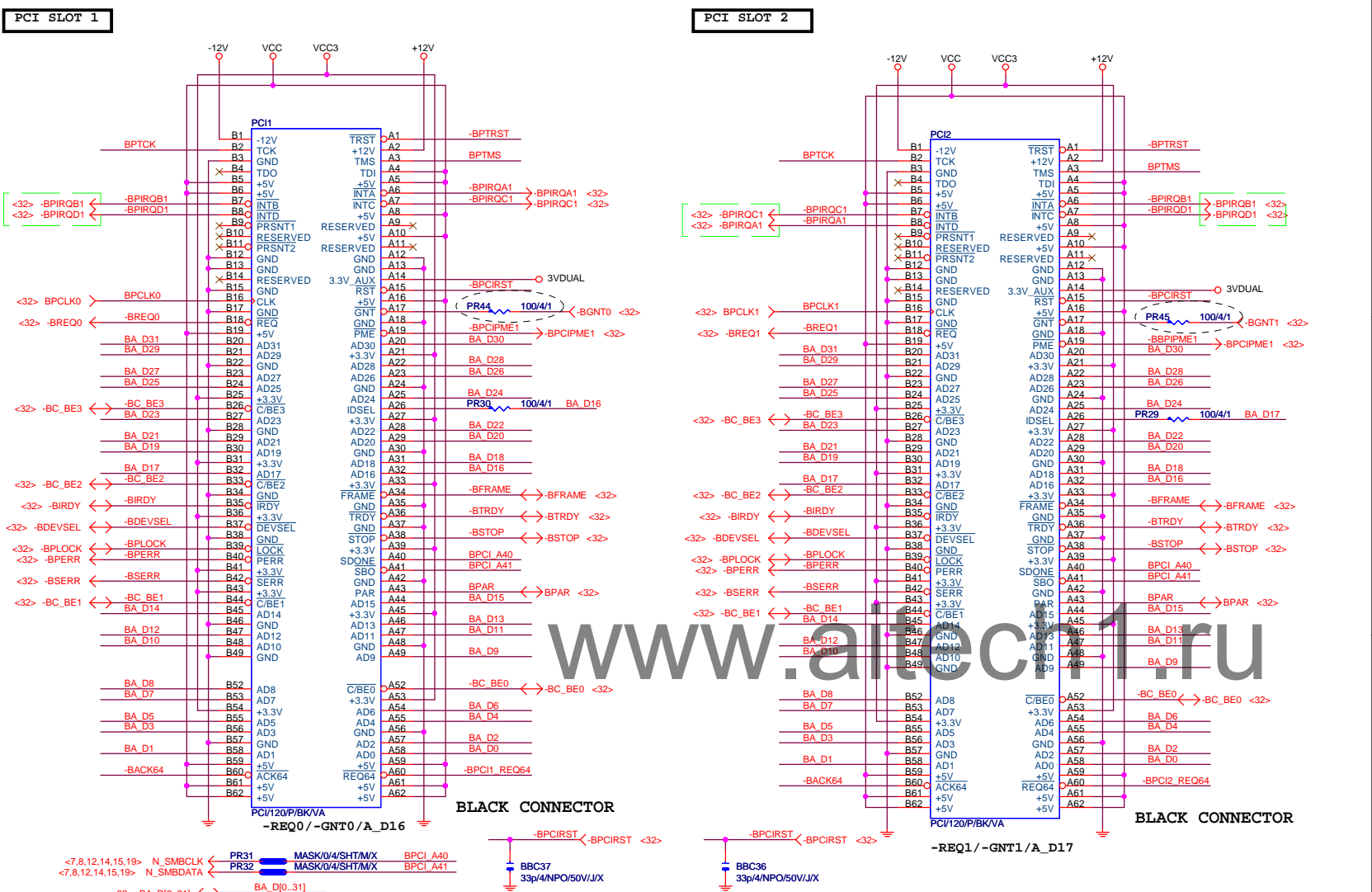


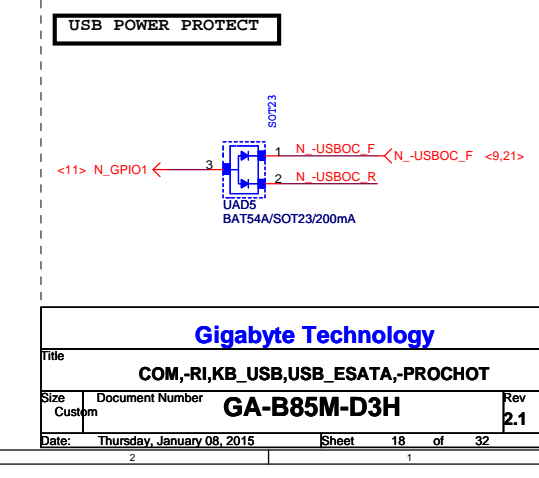
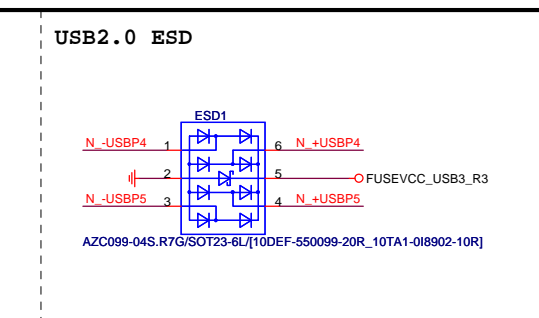
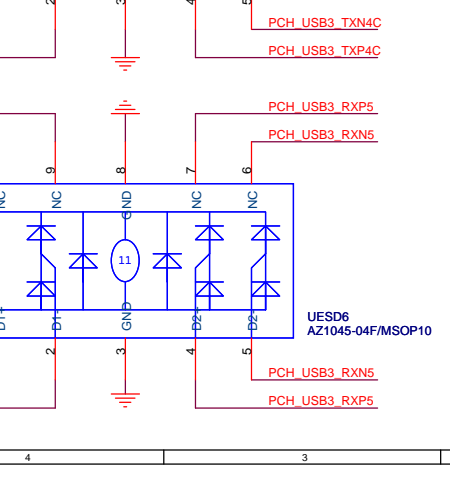
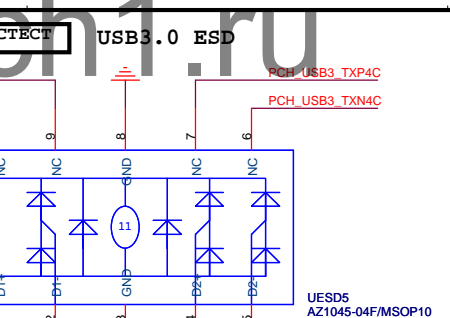
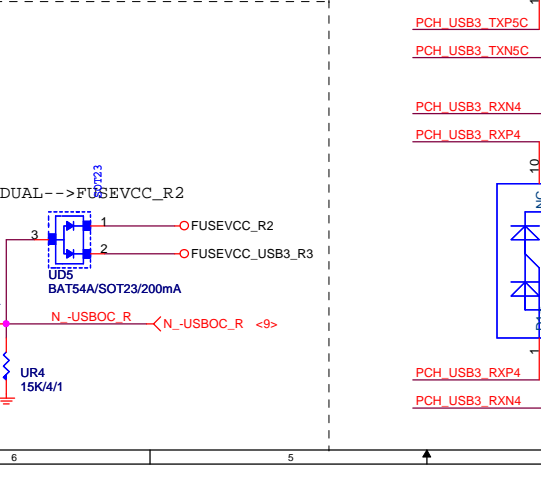
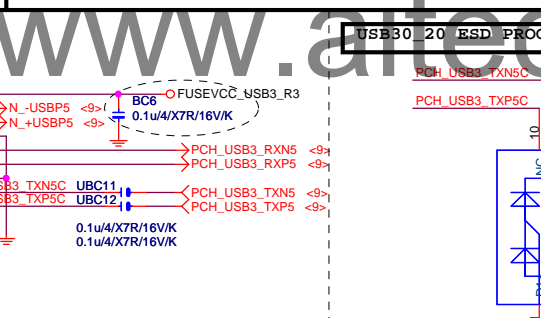
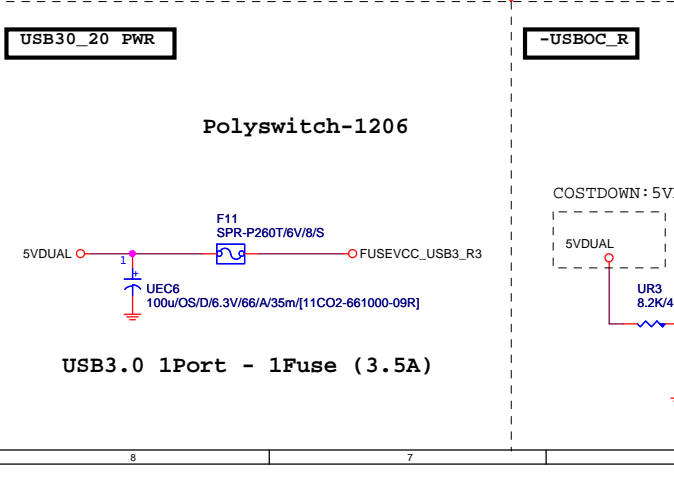
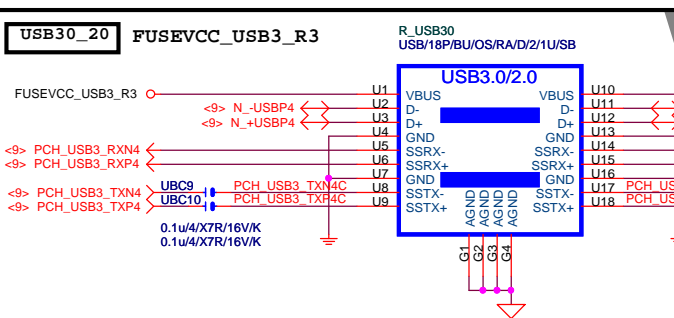
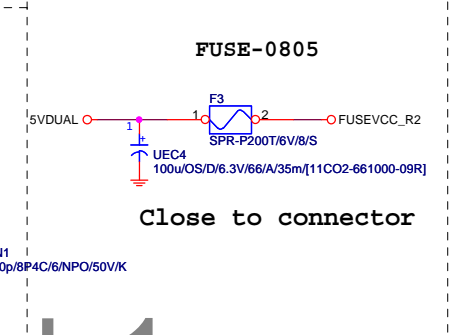
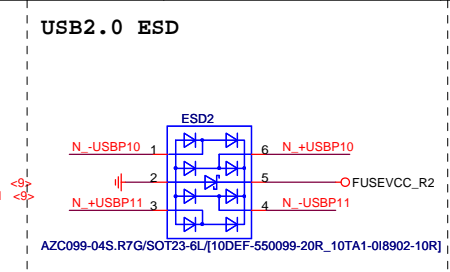
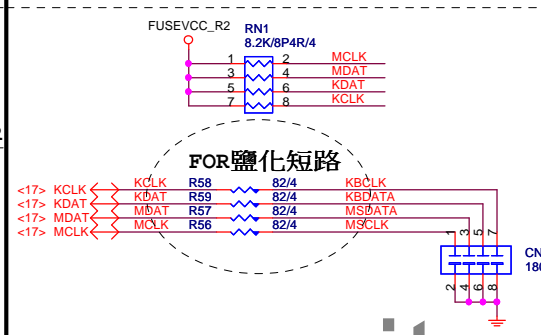
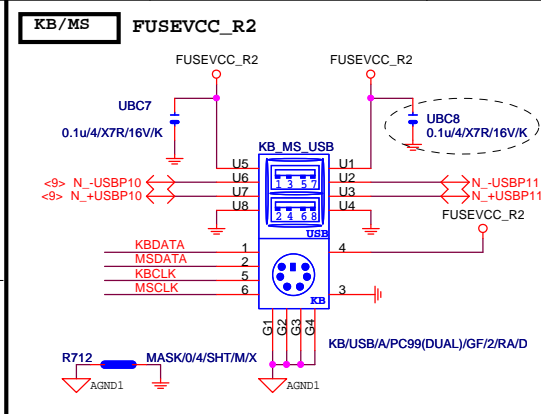
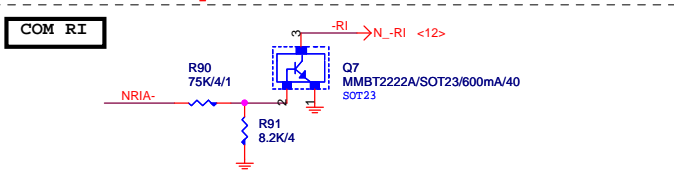
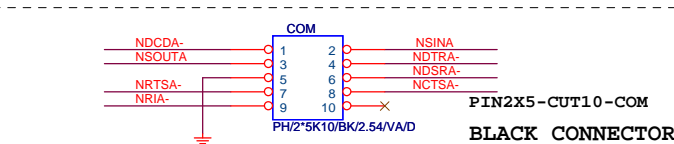
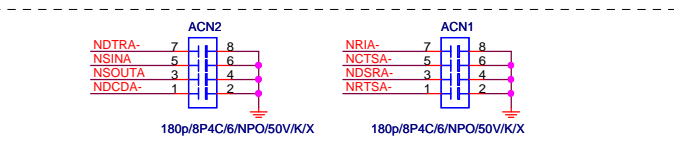
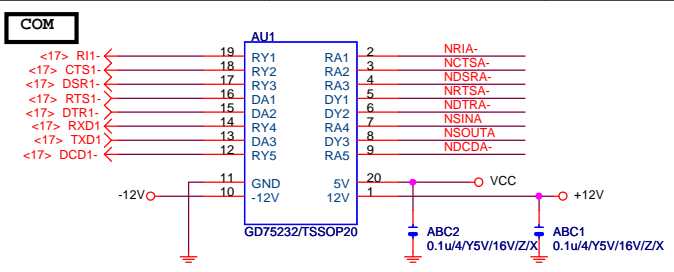
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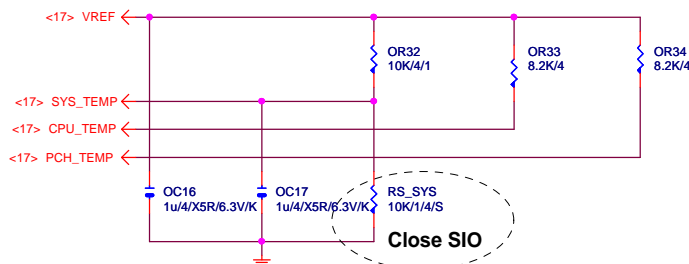
PCI-E/4X-65P/BK/LONG DOUBLE

BLACK CONNECTOR

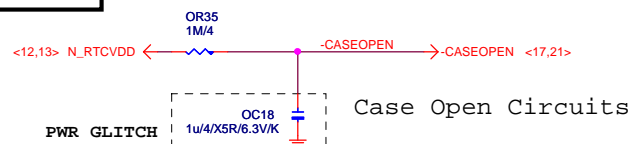




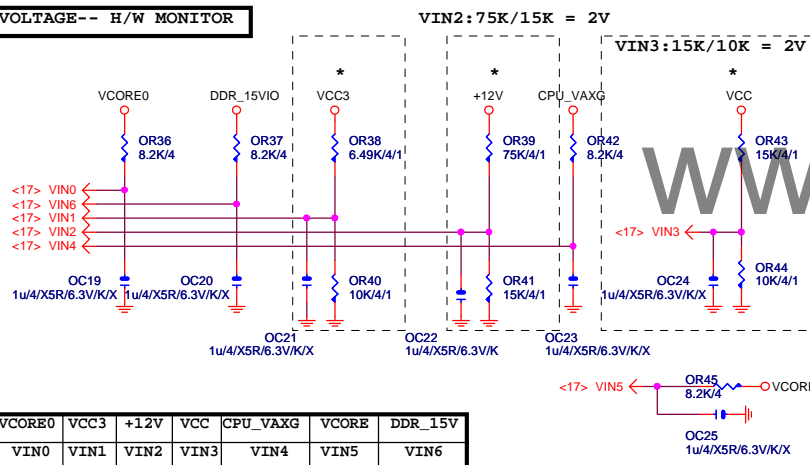
TEMP H/W MONITOR



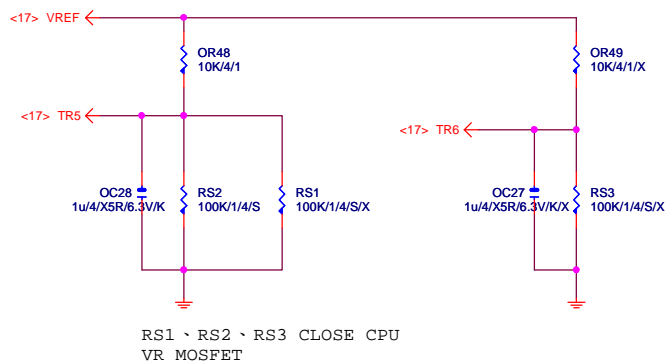
CASE OPEN



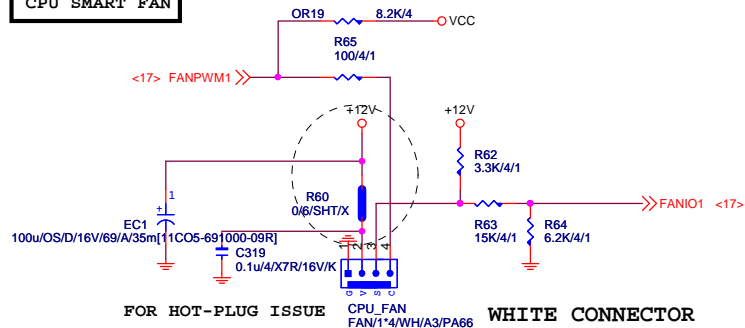
VOLTAGE-- H/W MONITOR



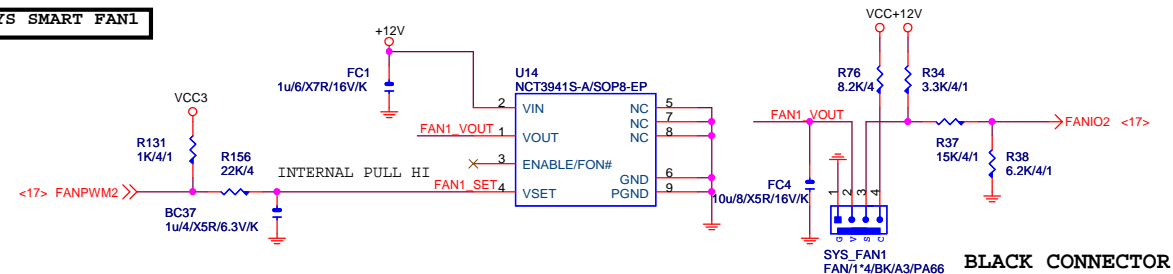
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VIN0	VIN1	VIN2	VIN3	VIN4	VIN5	VIN6



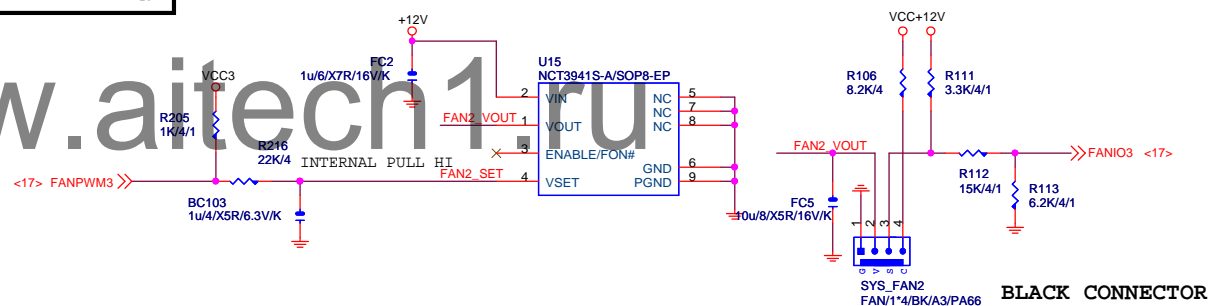
CPU SMART FAN



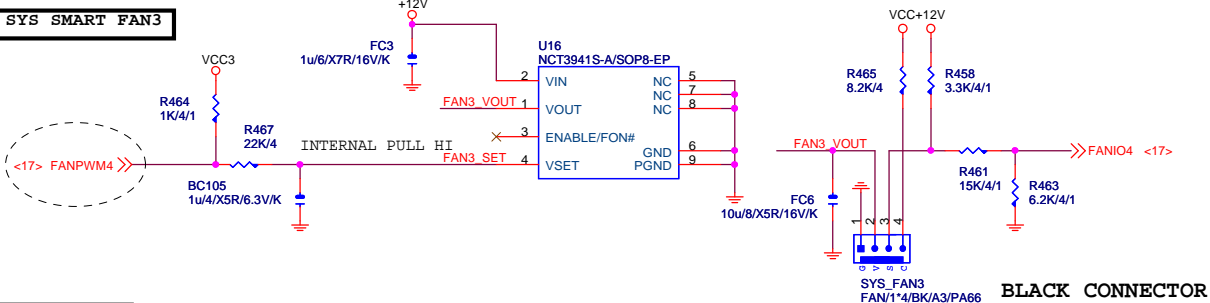
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SYS SMART FAN1
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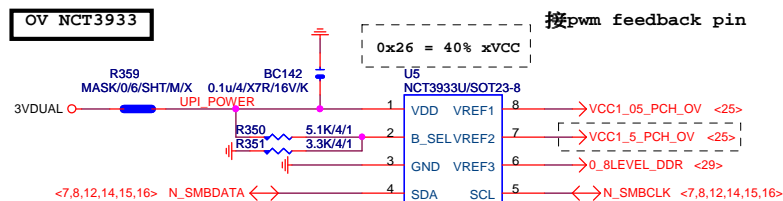
SYS SMART FAN2



SYS SMART FAN3

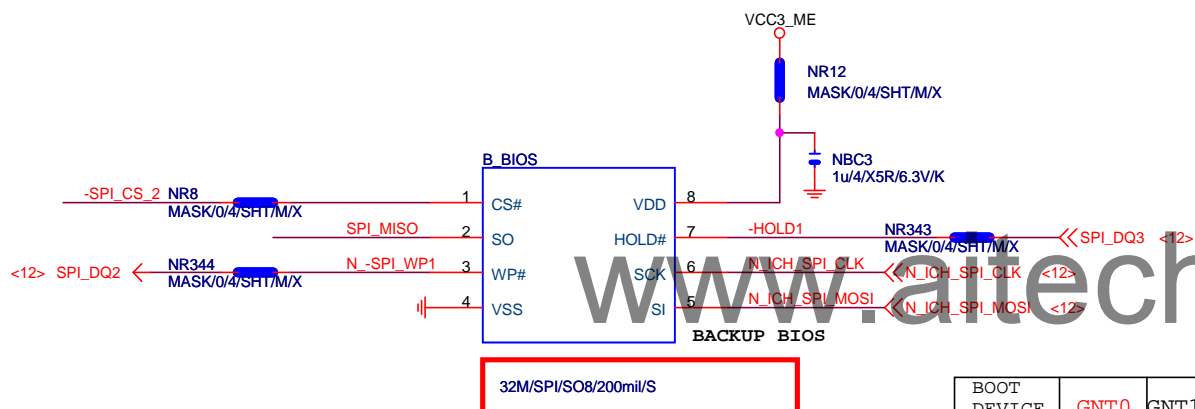
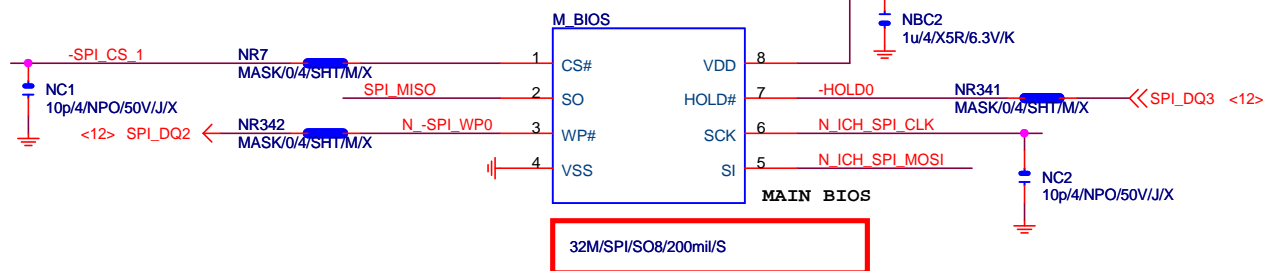


OV NCT3933



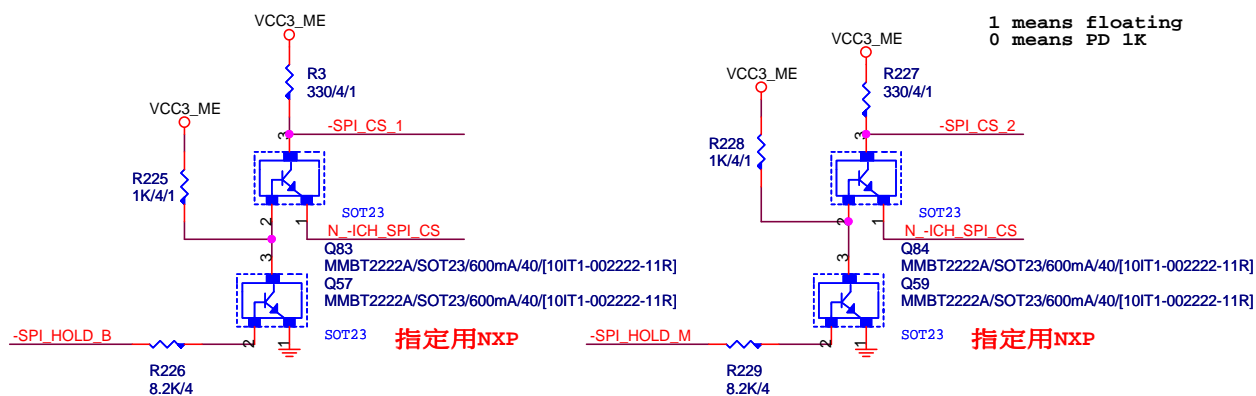
Gigabyte Technology

Title			
HWM,FAN CTRL,OV			
Size	Document Number		Rev
Custom	GA-B85M-D3H		2.1
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BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

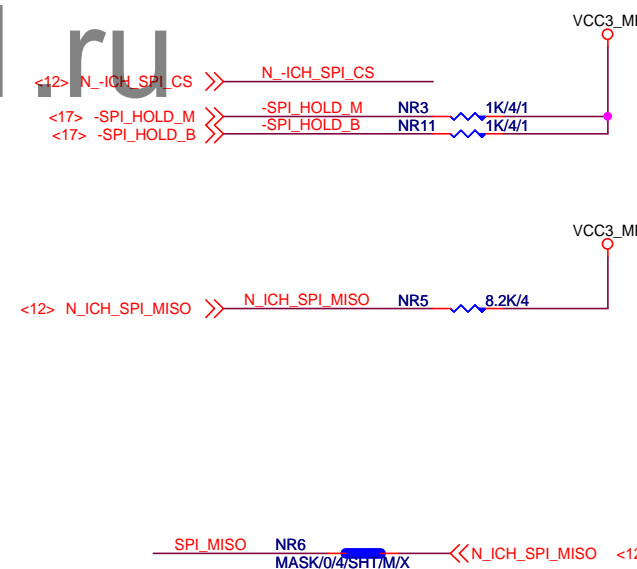
1 means floating
0 means PD 1K



BIOS DEBUG PORT

BIOS_PH R1.0 移除

MOSI For DMI RX Termination Voltage

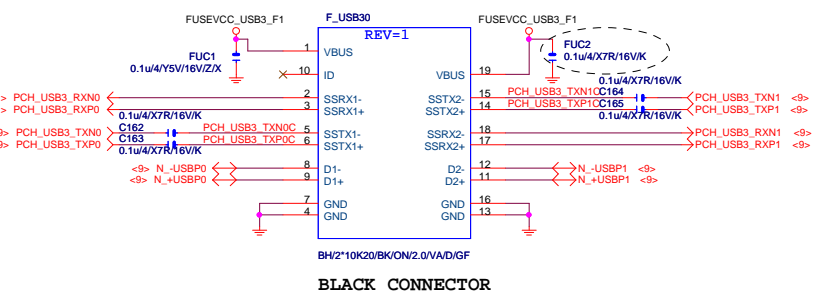


Gigabyte Technology

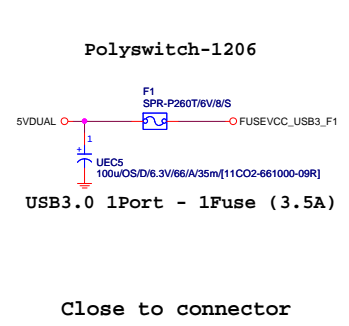
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Size Custom	Document Number	Rev
	GA-B85M-D3H	2.1
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F_USB30

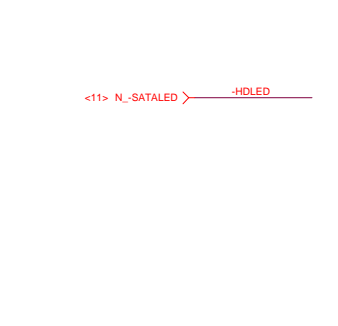
FUSEVCC_USB3_F1



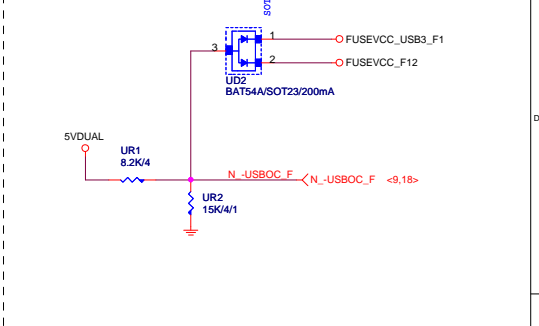
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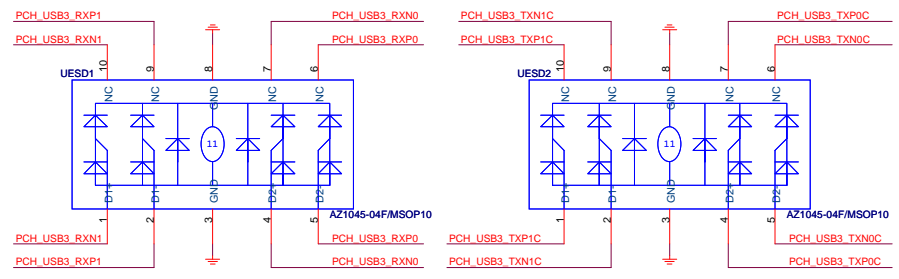
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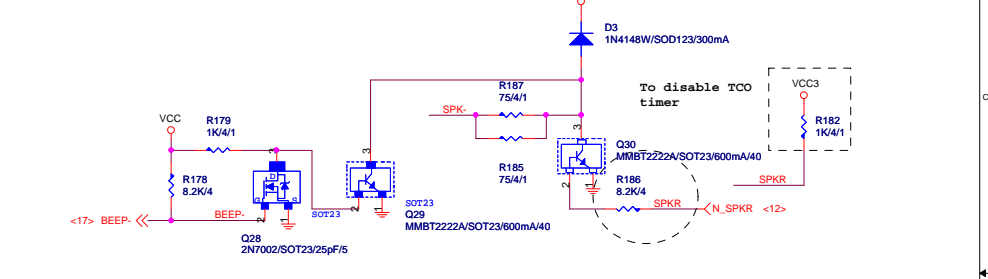
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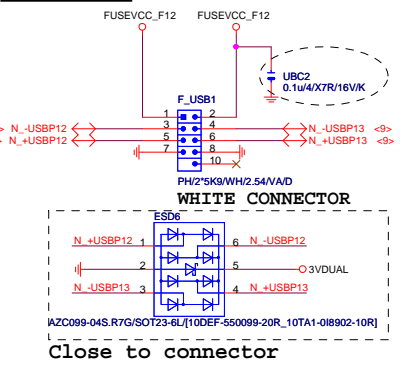


SPKR



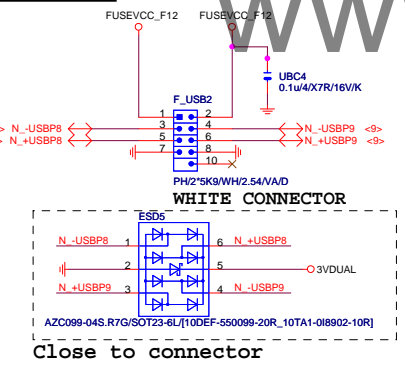
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FUSEVCC_F12

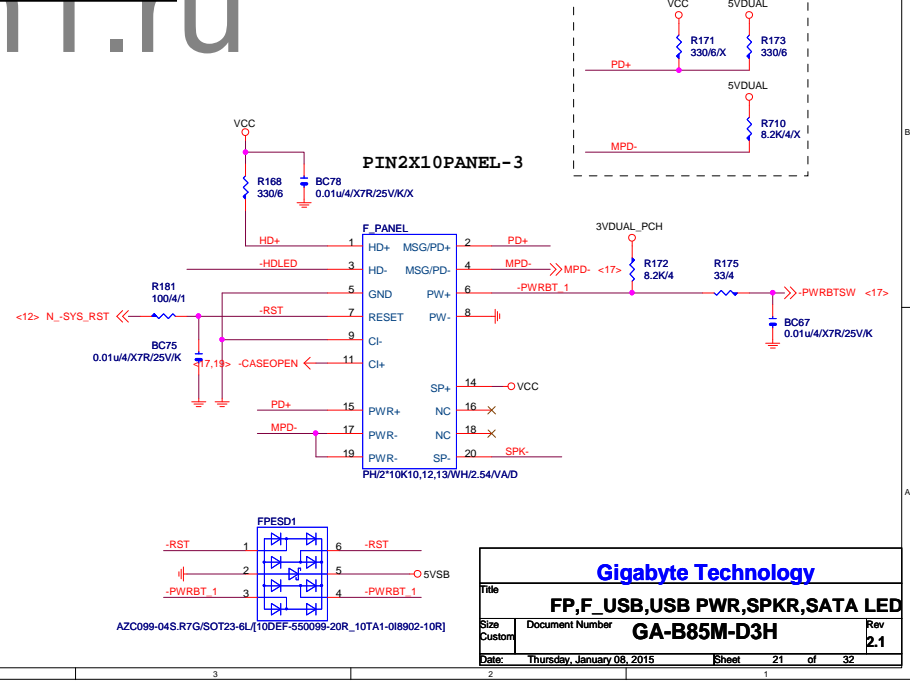


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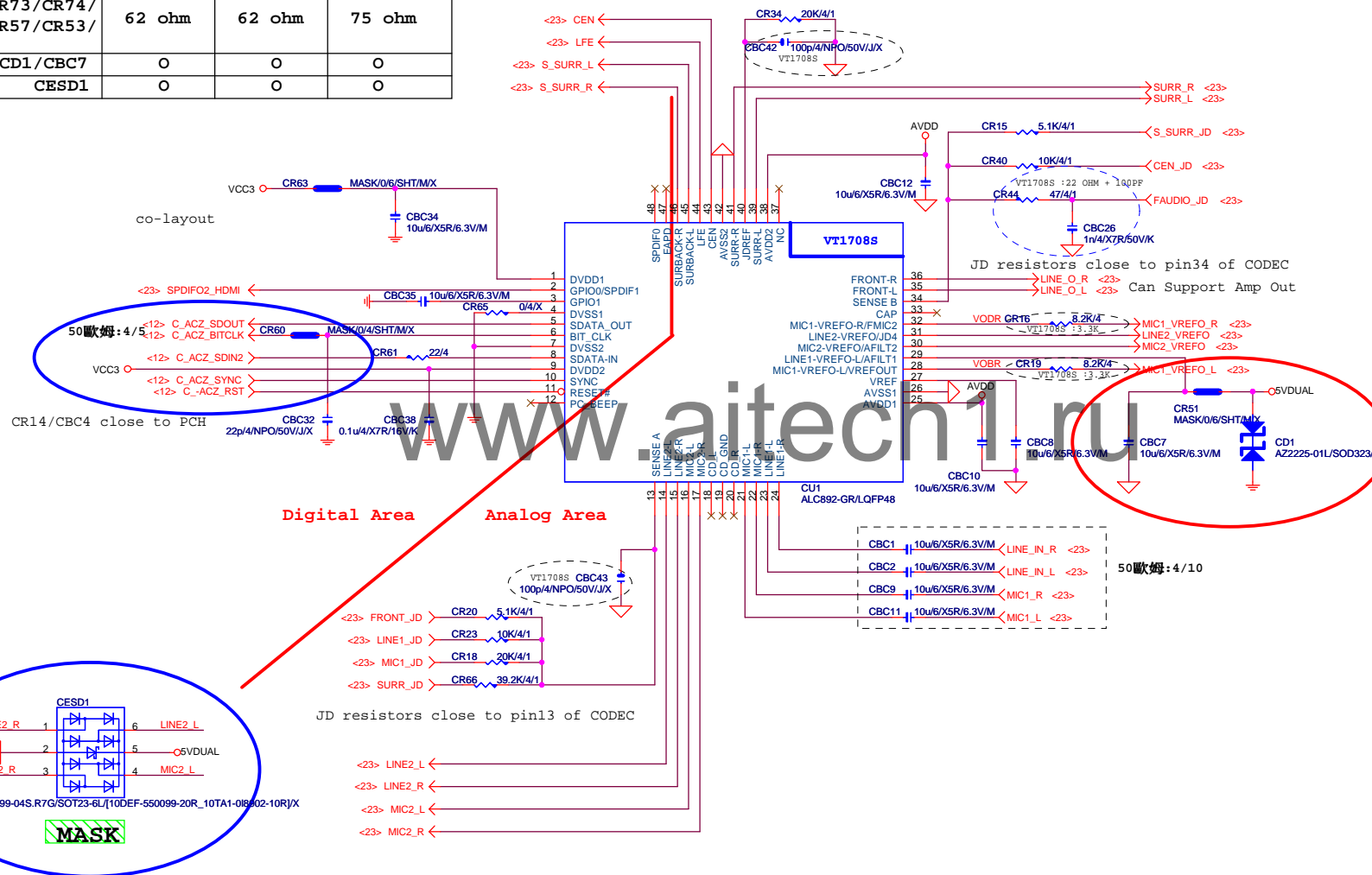
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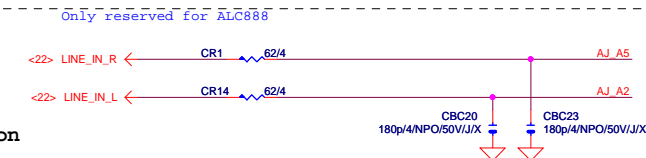
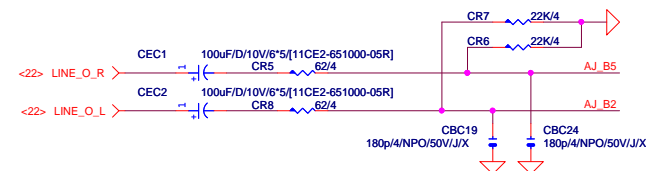
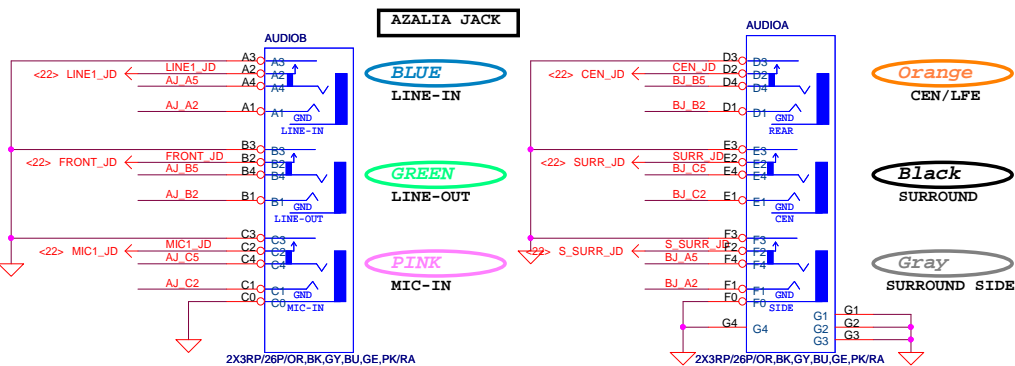
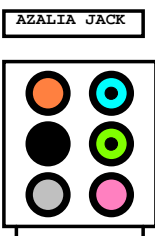
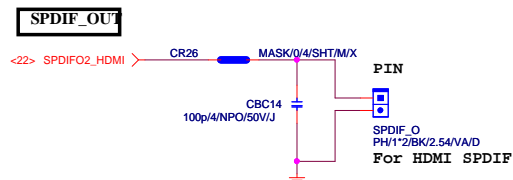
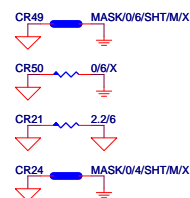


INTEL FRONT PANEL



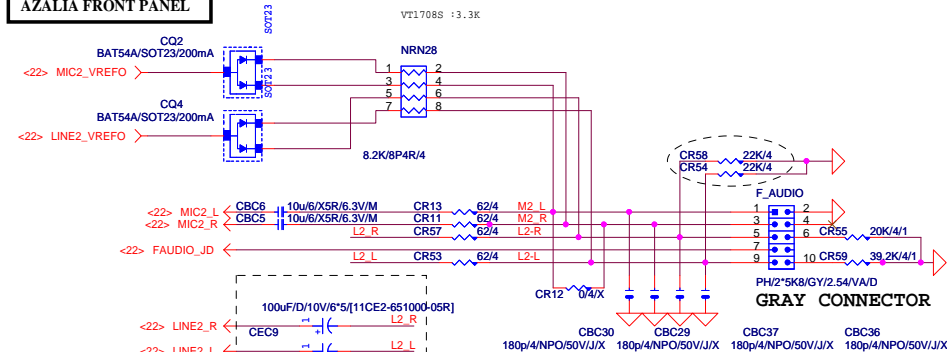
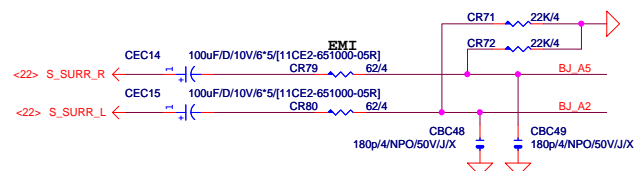
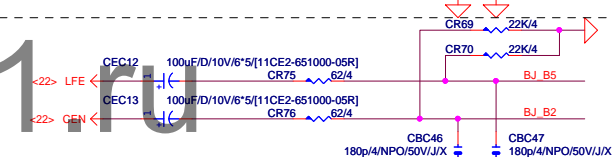
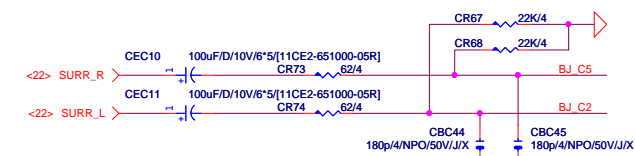
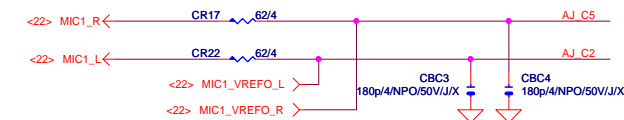
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CR44/CBC26	47ohm+1nF	47ohm+1nF	22ohm+100P
CBC42/CBC43	X	X	100P/4
CR6/CR7/CR58/CR54/ CR67/CR68/CR69/CR70	22K/4	22K/4	10K/4/1
CR5/CR8/CR1/CR14/ CR17/CR22/CR73/CR74/ CR13/CR11/CR57/CR53/ CR75/CR76	62 ohm	62 ohm	75 ohm
CR51/CD1/CBC7	O	O	O
CESD1	O	O	O

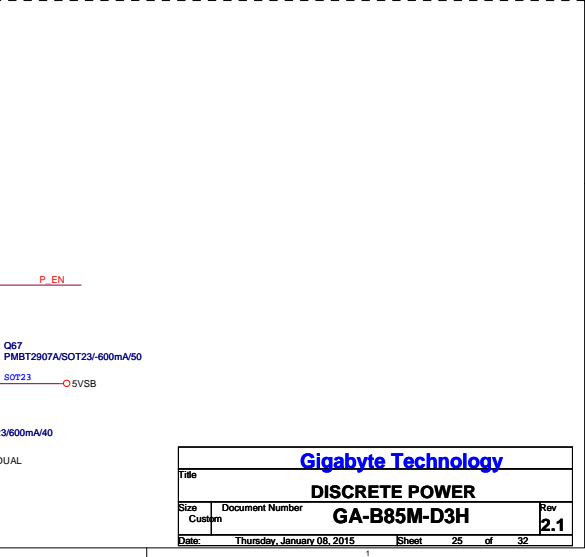
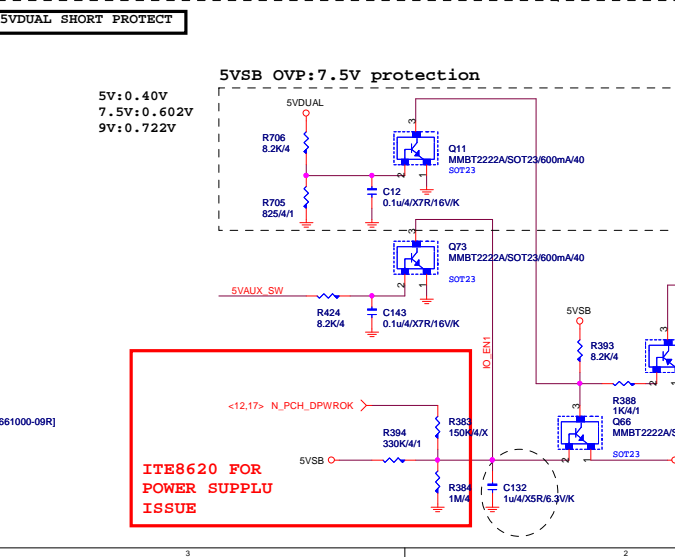
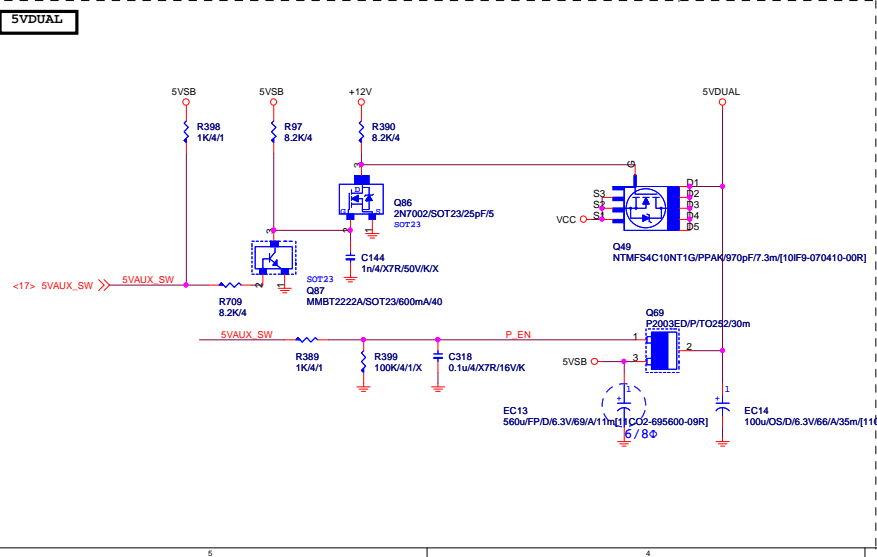
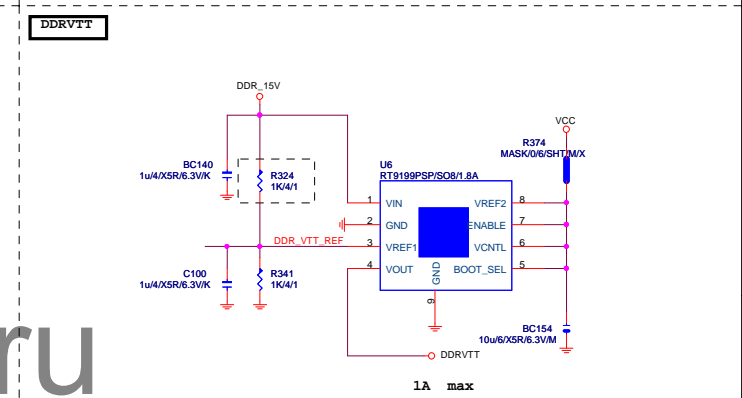
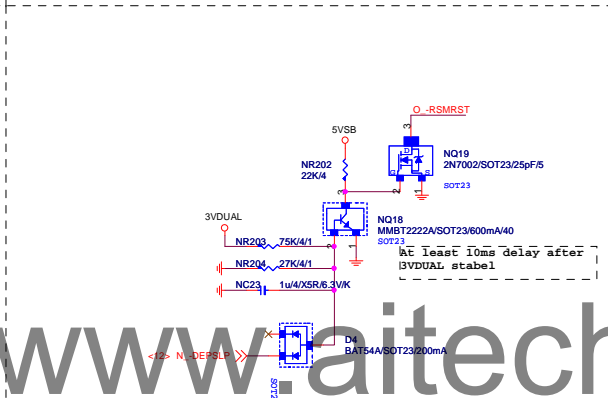
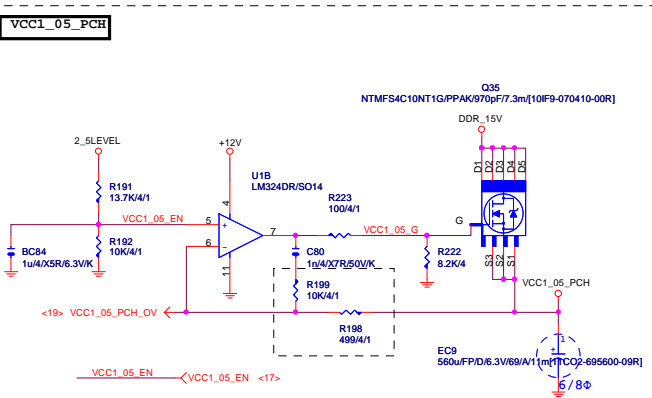
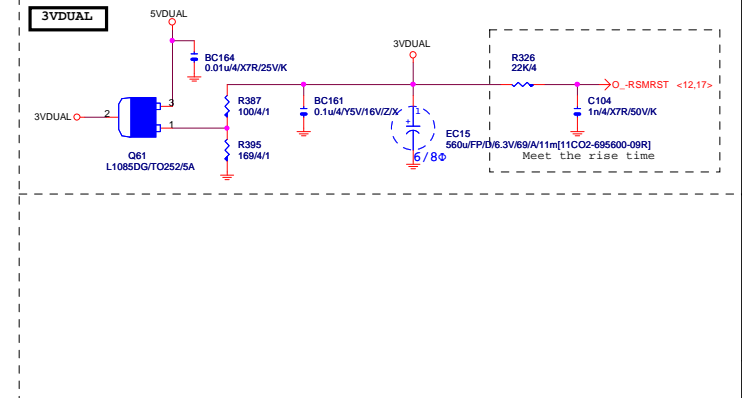
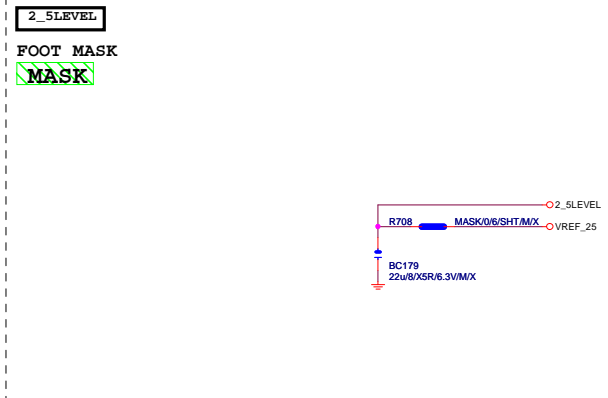
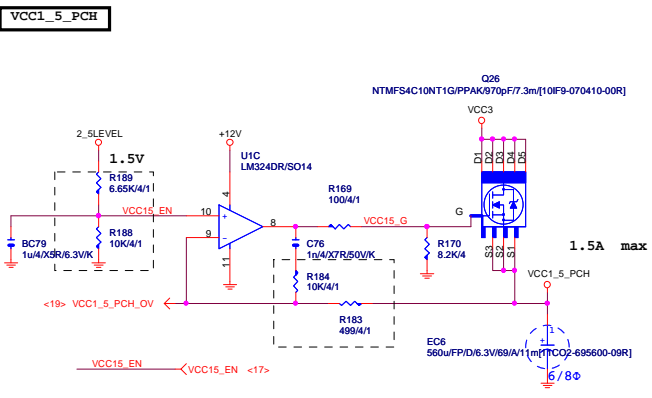




Verify MIC function
in LINE-in

For 889A/888

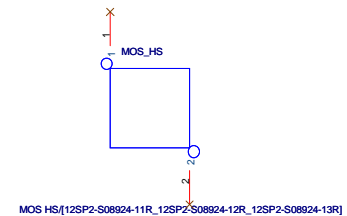
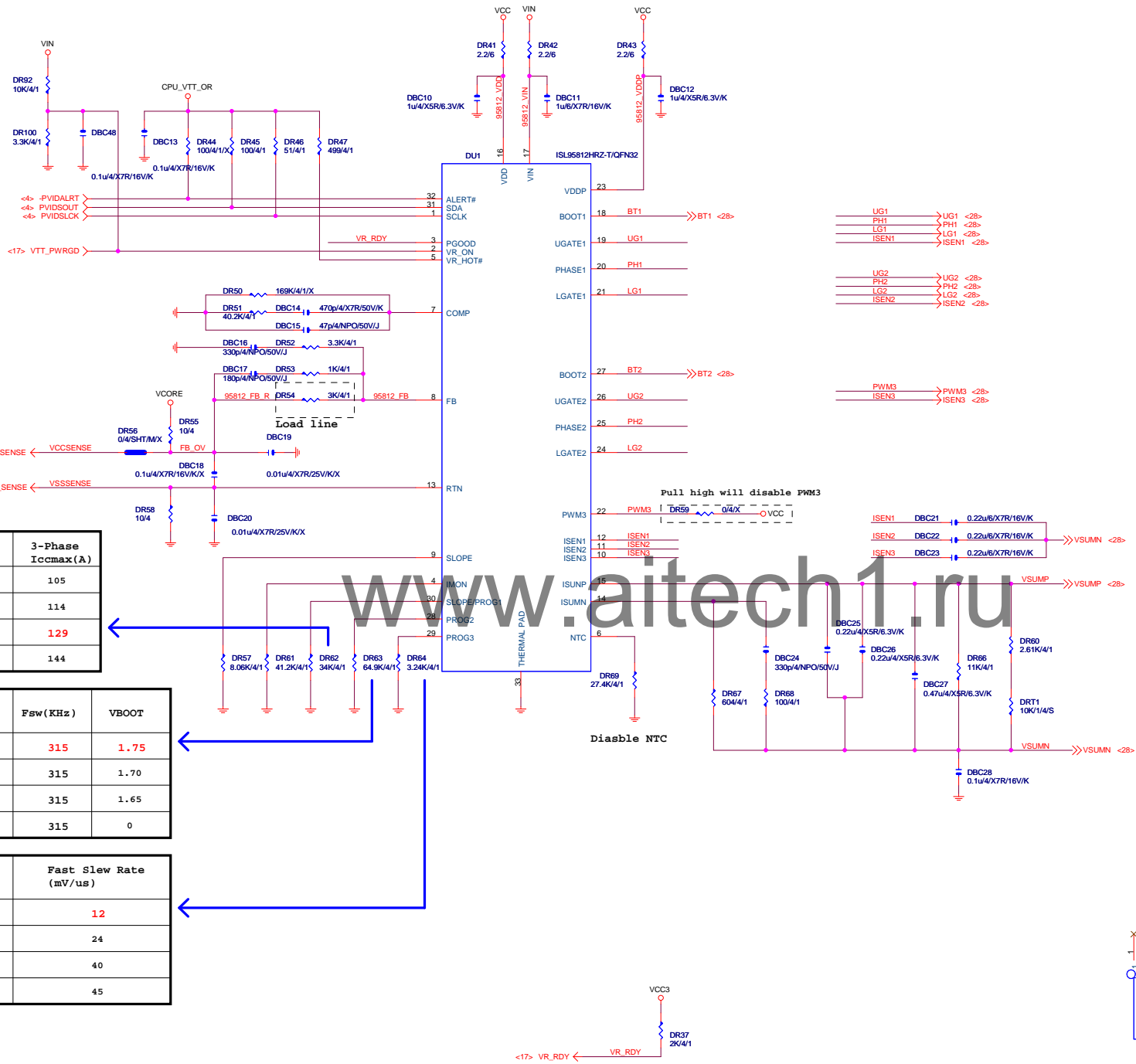




R_PROG1 (Kohm)	3-Phase Iccmax(A)
24.9	105
28.7	114
34.0	129
42.2	144

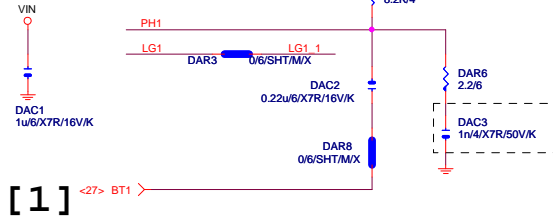
R_PROG2 (Kohm)	Fsw(KHz)	VBOOT
64.9	315	1.75
73.2	315	1.70
80.6	315	1.65
90.9	315	0

R_PROG3 (Kohm)	Fast Slew Rate (mV/us)
3.24	12
5.76	24
9.31	40
13.3	45

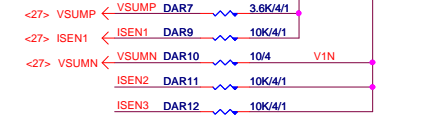
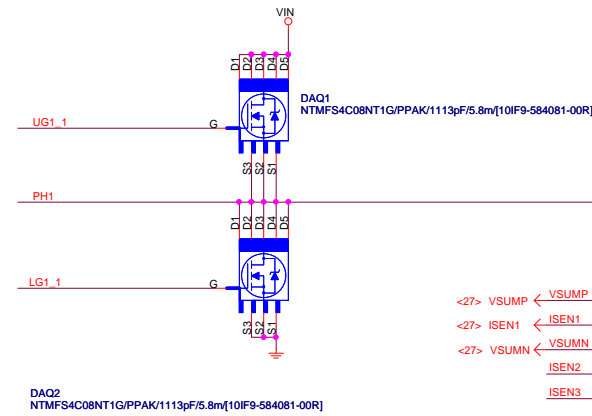


MOS HS[12SP2-S08924-11R_12SP2-S08924-12R_12SP2-S08924-13R]

PHASE 1

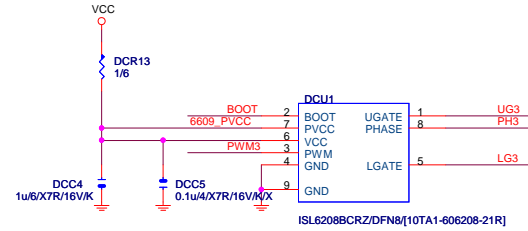
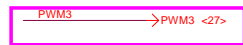


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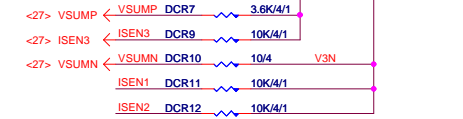
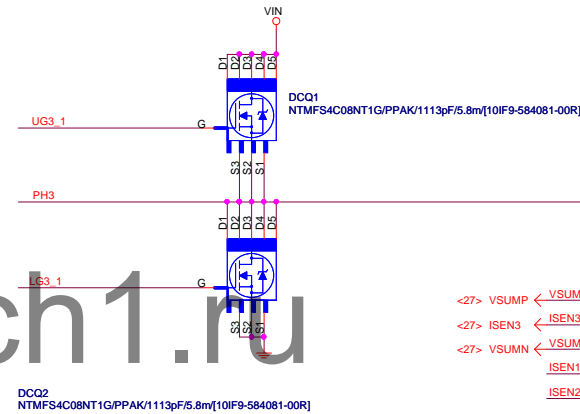
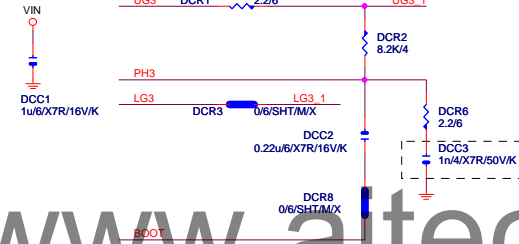


Close to PWM

PHASE 3

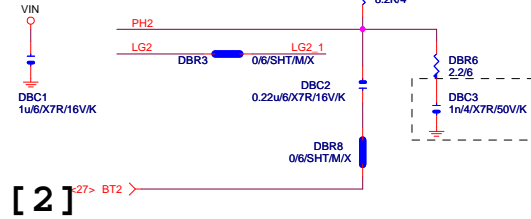
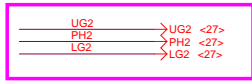


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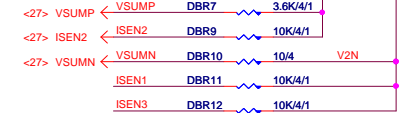
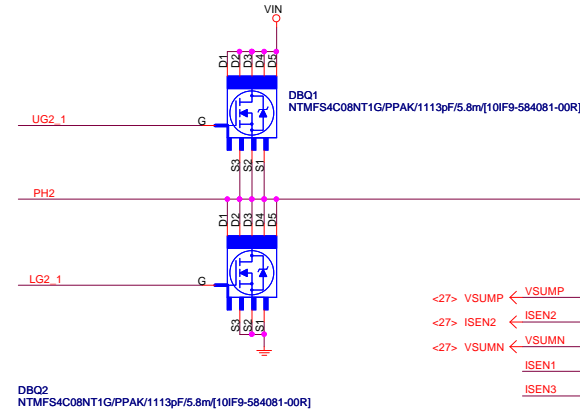


Close to PWM

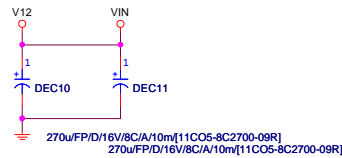
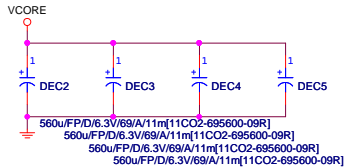
PHASE 2



[2] <27> BT2

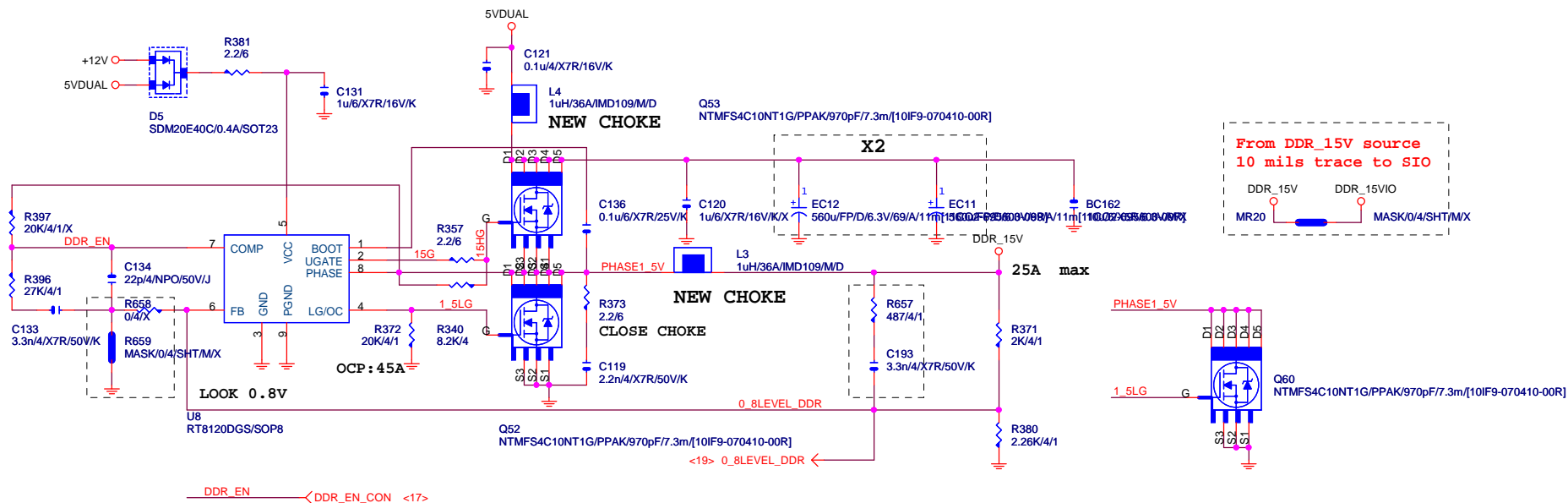


Close to PWM



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CPU CORE VR-2			
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DDR15V



PWR SEQ

VIN=5V, VOUT=1.5V, IOUT=25A, P
IRMS=11.45A
560uF/FP/D/6.3V/688m RIPPLE

VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
IRMS=11.45A
560uF/P/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85°C), 1(105°C)
VIN Ripple current=4.7X1.7=7.99A(85°C)
-->故固態電容須2X7.99=15.98>11.45A

$$\begin{aligned} \text{Rocset} &= (\text{Iocp} * \text{Lgate}, \text{rdson}) / \text{Iocset} \\ \text{Rocset} &= (45\text{A} * 6.7\text{mOhm}) / 10\text{uA} = 30\text{K} \\ \text{Iocset} &= 10\text{uA} \end{aligned}$$

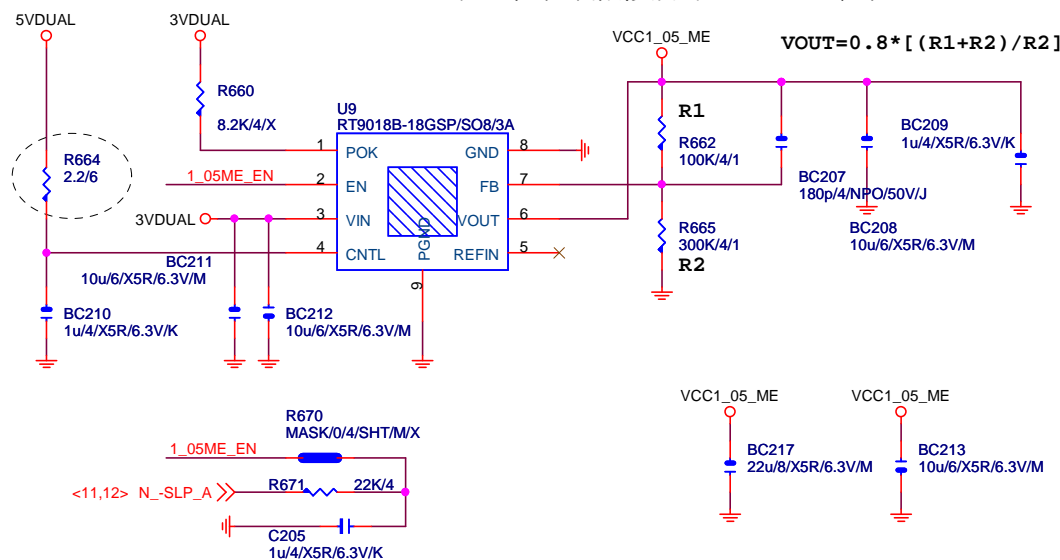
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<p>Title</p>			
<p align="center">DDR POWER</p>			
Size	Document Number	GA-B85M-D3H	Rev
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VCC1_05_ME FOOT MASK

Z97 N/A

【技術通報R&D技術通報156】

(RICHTER), (NUVOTON), (EMC)做共用
PIN7分壓阻值須做修改為100K以上電阻值



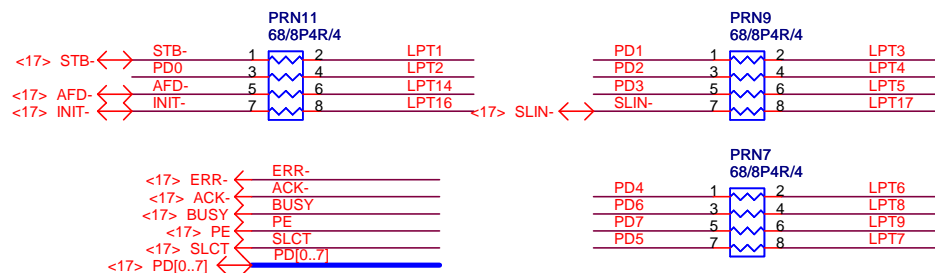
Second source

EM5103 - 10GL2-305103-01R

NCT3730S -

10GL2-303730-01R

LPT PORT

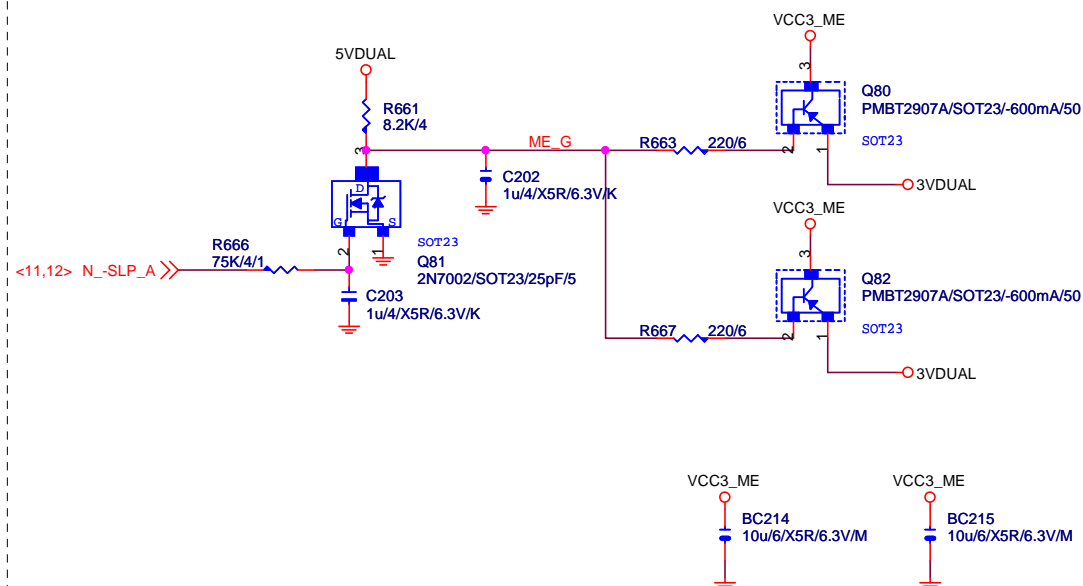


【技術通報R&D技術通報151】

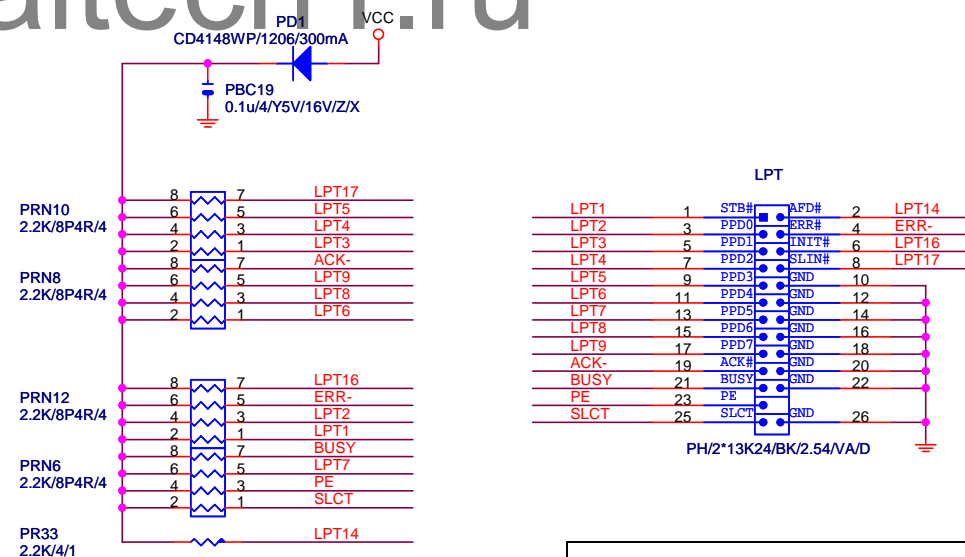
33ohm Change to 68ohm

VCC3_ME	FOOT MASK
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Z97 N/A



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Title	Author	Year	Journal	Volume	Issue	Page
1. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	1-15
2. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	16-30
3. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	31-45
4. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	46-60
5. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	61-75
6. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	76-90
7. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	91-105
8. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	106-120
9. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	121-135
10. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	136-150

LPT

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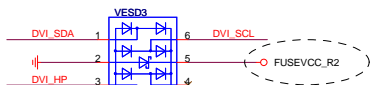
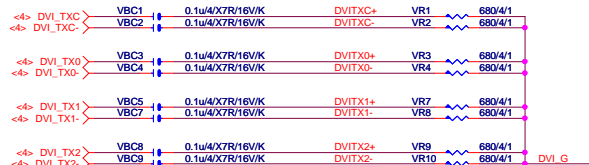
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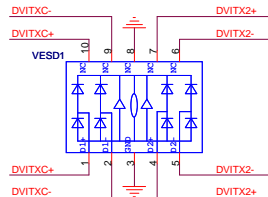
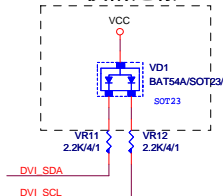
DVI LEVEL SHIFT



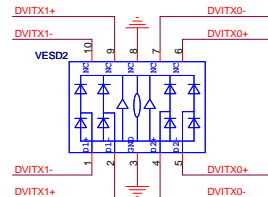
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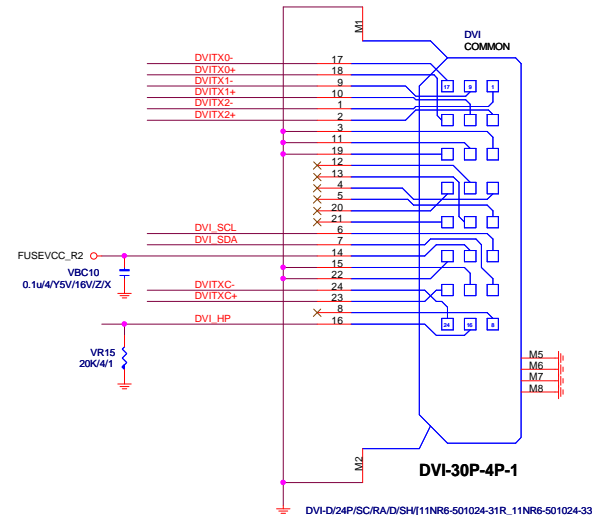
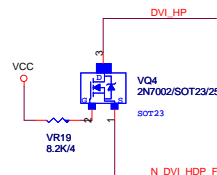
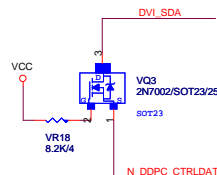
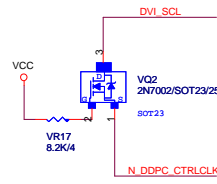
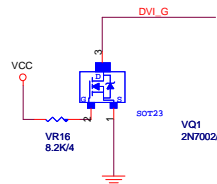
R&D技術通報 162



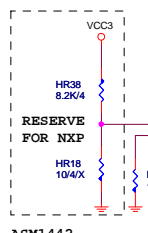
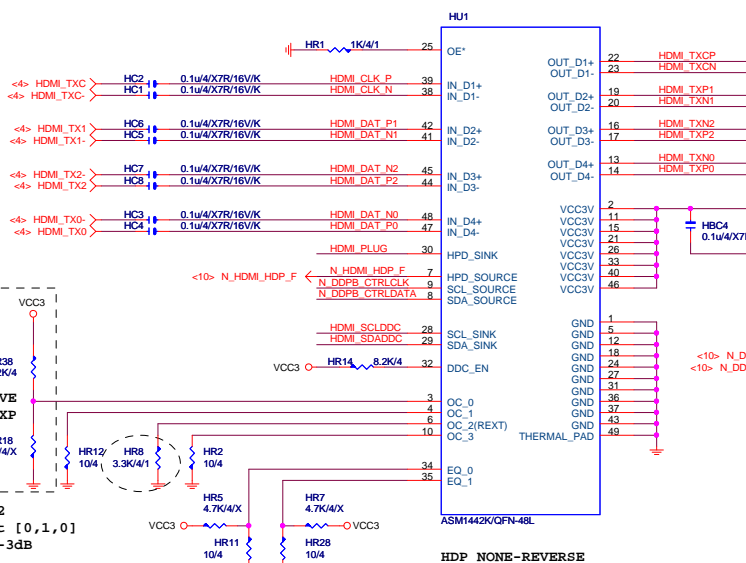
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Close to connector



HDMI LEVEL SHIFT



ASMI1442

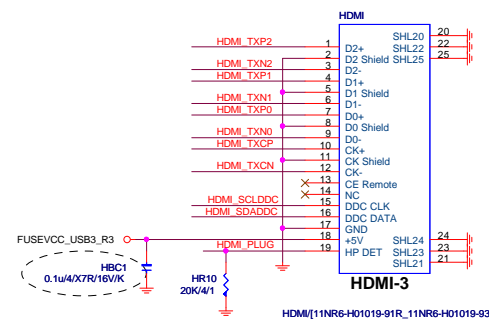
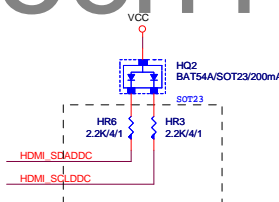
Default [0,1,0]

450mv, -3dB

ASMI1442 Default [0,0] 3dB

[0,1]6dB

HDP NONE-REVERSE



HDMI-3

HDMI[11NR6-HD1019-91R_11NR6-HD1019-93R]

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